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INSTRUCTION MANUAL

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KEPCO

PAT

MODULAR

POWER SUPPLIES

Model PAT 15-1.5

Serial No. P. 109216

INSTRUCTION MANUAL

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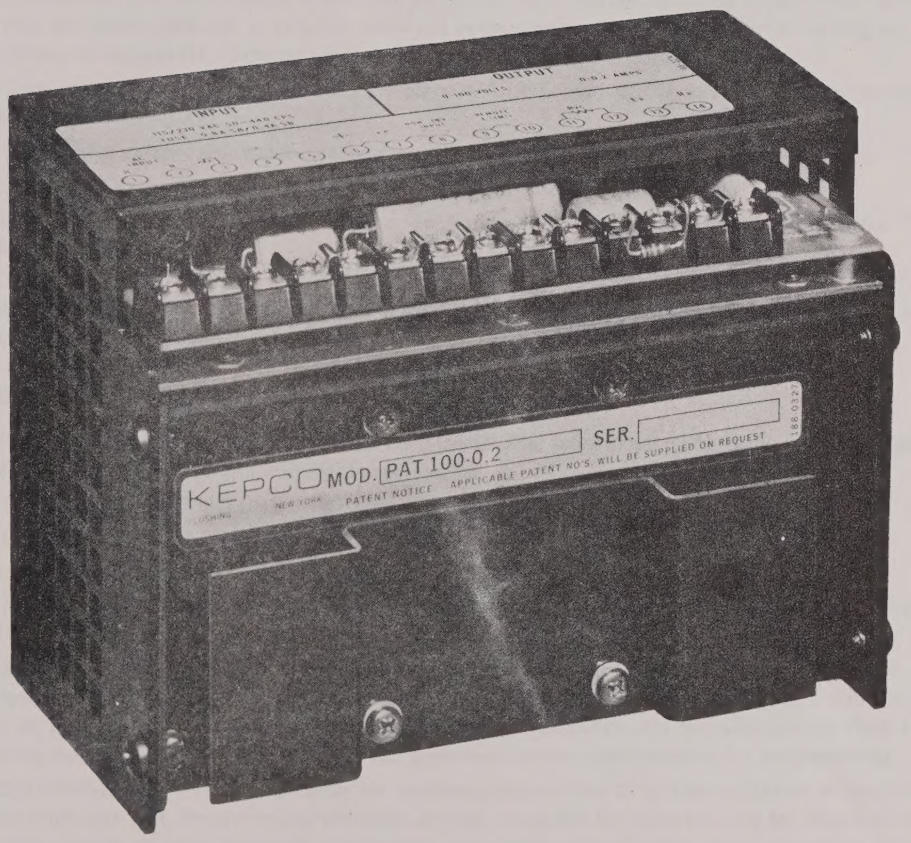


FIG. 1-1 PAT POWER SUPPLY (TYPICAL).

SECTION I – INTRODUCTION

1-1 SCOPE OF MANUAL

1-2 This manual contains instructions for the installation, operation, and maintenance of the Kepco PAT Series of power supplies.

1-3 GENERAL DESCRIPTION

1-4 The Kepco PAT Series of ***voltage regulated power supplies*** consists of a group of modular units with identical outside dimensions (refer to FIGS. 1-1, 1-2) and electrical specifications except as noted in TABLE 1-1.

1-5 Features for the Kepco PAT Series include short-circuit proof design by means of an extremely sharp current limit circuit, provisions for full-range programming and all silicon design with conservatively rated components for added reliability. The NPN output transistors are driven by a monolithic, linear IC amplifier. Kepco PAT modules may be readily adapted to provide stabilized ***output current*** by adding external sensing and control resistors. (Refer to Section III.) Options for the Kepco PAT Series power supplies include:

a) Built-in voltage control rheostat. (Models carry a 4 or 5 place suffix with their model number; e.g., "Model PAT 7–2–8000.")

b) Built-in bridge current adjustment. (Models with suffix "R;" e.g., "Model PAT 21–1R.")

c) Built-in zero adjustment. (Models with suffix "E;" e.g., "Model PAT 15–1.5E.")

d) Special reference source (zener diode) for low temperature coefficient. (Models with suffix "T;" e.g., "Model PAT 40–0.5T.")

NOTE: Options a), b), and c) may be installed by the user. Installation instructions are given in Section V of this manual.

1-6 SPECIFICATIONS, GENERAL

a) INPUT: 105 to 125V a-c or 210 to 230V a-c (selectable, refer to par. 2-5), 50 to 440 Hz, single phase. See TABLE 1-1 for the input current on each model.

b) OPERATING TEMPERATURE: -20°C to +71°C.

c) STORAGE TEMPERATURE: -40°C to +85°C.

d) COOLING: Convection.

e) ISOLATION VOLTAGE: A maximum of 500V (d-c or p-p) can be connected between chassis and either output terminal.

f) CONTROLS: PAT modules are ordinarily supplied without controls. External fixed or variable programming resistors are used to control the output. Optionally, on special order, a built-in trimmer can be provided for up to 20 volts adjustment range.* Stability and temperature coefficient criteria require the use of high quality, low T.C. (20 ppm min.) wirewound elements for programming.

g) PROGRAMMING: Terminals provide for resistive programming of voltage or current at approximately 1000 ohms per volt. Programming terminals are also provided for programming by means of remotely located voltage or current signals. Optionally, on special order, built-in trimmers can be provided to adjust the control ratio over a 10% range and to set the power supply to exactly zero volts, so that exact value fixed programming resistances can be used conveniently. (See pars. 1-5b and 1-5c.)

h) CURRENT LIMIT CONTROL: A single-turn control provides adjustable current limiting from 10% to 105% of rated full load current.

*Inquire at factory.

i) SHORT-CIRCUIT PROTECTION: Unique current limiting circuitry permits continuous operation into a short circuit without the aid of fuses, circuit breakers, or relays. Output returns to the operating voltage when the overload is removed.

j) REMOTE ERROR SENSING: Error-sensing terminals enable specified voltage regulation to be maintained directly at the load by compensating for voltage drops up to 0.5 volts across each load supply lead.

k) SERIES/PARALLEL OPERATION: Series connection of identical units is possible up to the specified isolation voltage limit, and provided the necessary precautions are taken. (See par. 3-33.) PAT supplies can be parallel connected and will share the load by means of their current limiting feature. (See par. 3-36.)

l) COOLING: Heat removal is by natural convection without blowers.

m) OVERSHOOT: No output voltage overshoot from turn-on, turn-off, or power failure for output settings above 25% maximum rated output voltage. Below 25%, output voltage overshoot is a function of load current and is negligible for loads in excess of 10% of the maximum rated load current.

1-7 SPECIFICATIONS, ELECTRICAL PERFORMANCE

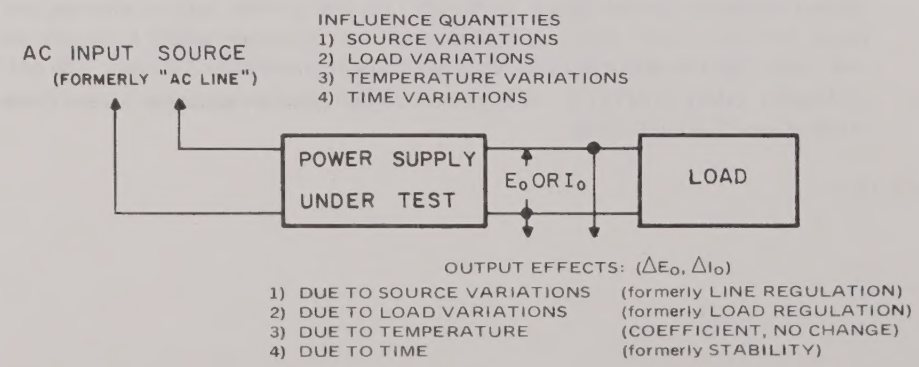
a) Refer to TABLES 1-1 and 1-2.

MODEL	D-C OUTPUT RANGE		OUTPUT IMPEDANCE OHMS + MICROHENRYS			MAX. INPUT AMPS
	VOLTS	AMPS	D-C to 100 Hz	100 Hz to 1 kHz	1 kHz to 100 kHz + μH	at 125 V A-C
PAT 7–2	0–7	0–2	0.2×10^{-3}	0.02	0.1 + 1	0.50
PAT 15–1.5	0–15	0–1.5	0.5×10^{-3}	0.02	0.1 + 1	0.65
PAT 21–1	0–21	0–1	1×10^{-3}	0.02	0.1 + 1	0.45
PAT 40–0.5	0–40	0–0.5	4×10^{-3}	0.02	0.1 + 1	0.40
PAT 72–0.3	0–72	0–0.3	12×10^{-3}	0.02	0.1 + 1	0.45
PAT 100–0.2	0–100	0–0.2	25×10^{-3}	0.02	0.1 + 1	0.45

TABLE 1-1 OUTPUT SPECIFICATIONS AND A-C INPUT CURRENT REQUIREMENTS.

NOTE: Kepco has adopted new technical terms recommended by the International Electrotechnical Commission (IEC). These terms replace or supplement previously used expressions, mainly to avoid difficulties in translation and prevent erroneous interpretations at home and abroad.

In this instruction manual Kepco discontinued the use of the specifications entitled "line regulation" and "load regulation" because of the long standing (and misleading) connotation that a power supply regulates the line or the load. Instead, Kepco will follow the recommendation of the IEC and speak of the "Output Effects, caused by changes in the Influence Quantities." The "Output Effects" are specified either as a percentage change referred to the maximum specified output voltage (E_O) or current (I_O) or as an absolute change (ΔE_O, ΔI_O) directly in millivolts or milliamperes or both. The "Influence Quantities" are the "Source Variations" (formerly a-c line variations), the changes in load, temperature or time as previously specified. The illustration below will clarify the use of the new terminology.


INFLUENCE QUANTITIES
1) SOURCE VARIATIONS
2) LOAD VARIATIONS
3) TEMPERATURE VARIATIONS
4) TIME VARIATIONS
AC INPUT SOURCE
(FORMERLY "AC LINE")
POWER SUPPLY UNDER TEST
E_O OR I_O
LOAD
OUTPUT EFFECTS: (ΔE_O, ΔI_O)
1) DUE TO SOURCE VARIATIONS (formerly LINE REGULATION)
2) DUE TO LOAD VARIATIONS (formerly LOAD REGULATION)
3) DUE TO TEMPERATURE (COEFFICIENT, NO CHANGE)
4) DUE TO TIME (formerly STABILITY)

SPECIFICATION	OUTPUT EFFECTS		VOLTAGE AMPLIFIER OFFSET VARIATIONS****		VOLTAGE-REFERENCE (Internal)****
	VOLTAGE MODE	CURRENT MODE***	OFFSET VOLTAGE ΔE_{io}	OFFSET CURRENT ΔI_{io}	
OUTPUT RANGE:	0–100% E_o max.	1 mA–100% I_o max.	———	———	Fixed 6.2V ±5%
SOURCE: 105–125/210–250V a-c	<0.0005%	<0.005%	<10 μV	<2 nA	0.0001%
LOAD: No load – full load	<0.005% or 0.2 mV*	<0.01%	<200 μV	<5 nA	———
TIME: 8-hours (drift)	<0.01% or 1 mV*	<0.02%	<20 μV	<2 nA	0.005%
TEMPERATURE: Per °C	<0.01%**	<0.02%**	<20 μV	<5 nA	0.005%
RIPPLE: (rms)	<0.1 mV	<0.02% of I_o max.	———	———	———

*NOTE: Whichever is greater.

**NOTE: Models with suffix "T" have 0.005% and 0.01% per °C temperature coefficients in the voltage and current mode respectively.

***NOTE: Current mode output effects (ΔI_o) are measured across the external sensing resistor. This resistor must be a high quality, wirewound unit, with a wattage at least 10 times the actual power dissipated, have a temperature coefficient of 20 parts per million or better and drop a sample voltage of 1.0 volt at the measuring current. A resistor built as a 4-terminal network is recommended. Current control is exercised with an external 20 ppm feedback resistor.

****Offset and reference variations (ΔE_{io}, ΔI_{io}, ΔE_r) contribute to output effect (ΔE_o) according to the equation:

$$\Delta E_o = \pm\Delta E_{ref} (R_f/R_i) \pm\Delta E_{io} (1 + R_f/R_i) \pm\Delta I_{io} (R_f)$$

where R_f is the feedback resistor and R_i is the input resistor from the signal reference. Use the offset variations to calculate output effects when selecting external input/feedback elements or if the internal reference is replaced by a programming signal.

TABLE 1-2 ELECTRICAL PERFORMANCE SPECIFICATIONS.

1-8 ACCESSORIES

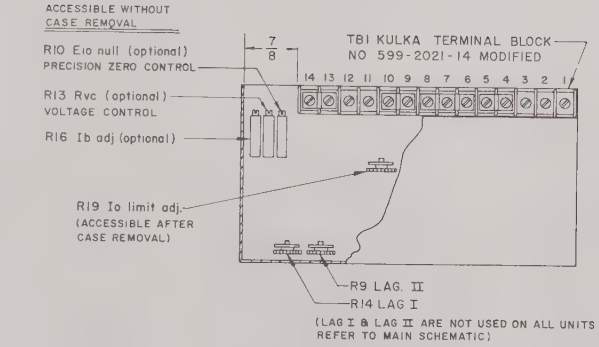
a) One-unit rack adapter, Kepco Model RA 26–1.

b) Two-unit rack adapter, Kepco Model RA 27–2.

c) Four-unit rack adapter, Kepco Model RA 28–4.

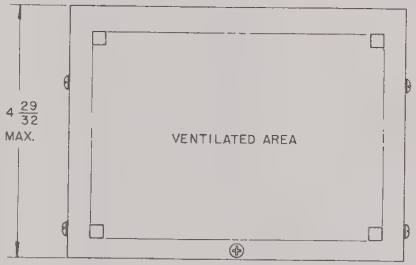
1-9 PHYSICAL SPECIFICATIONS

a) Refer to "Mechanical Outline Drawing," FIG. 1-2.


ACCESSIBLE WITHOUT CASE REMOVAL
R10 Eio null (optional) PRECISION ZERO CONTROL
R13 Rvc (optional) VOLTAGE CONTROL
R16 Ib adj (optional)
R19 Io limit adj. (ACCESSIBLE AFTER CASE REMOVAL)
7/8
TB1 KULKA TERMINAL BLOCK NO 599-2021-14 MODIFIED
14 13 12 11 10 9 8 7 6 5 4 3 2 1
R9 LAG II
R14 LAG I
(LAG I & LAG II ARE NOT USED ON ALL UNITS REFER TO MAIN SCHEMATIC)

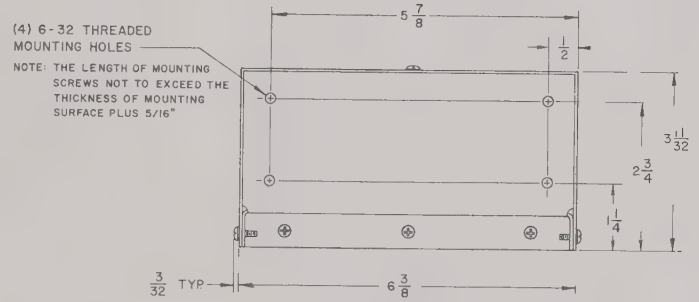
REAR OUTPUT TERMINAL FUNCTIONS:

1. AC (HOT)
2. AC (NEUTRAL)
3. AC (GROUND)
4. (—) ERROR SENSING
5. (—) OUTPUT
6. (+) OUTPUT
7. (+) ERROR SENSING
8. NON-INVERTING INPUT
9. } EXTERNAL CURRENT LIMIT CONTROL
10. }
11. EXT. VOLTAGE CONTROL } EXT. VOLTAGE CONTROL
12. INVERTING INPUT }
13. E_R (REF. VOLTAGE) } EXT. CONTROL CURRENT CAL.
14. R_R (REF. RESISTANCE) }



NOTES:

1. THIS DRAWING IS USED FOR THE MODELS PAT 7-2, PAT 15-1.5, PAT 21-1, PAT 40-0.5, PAT 72-0.3 & PAT 100-0.2
2. MATERIAL: CHASSIS: 11 GA. (.090) ALUMINUM
   COVER: 16 GA. (.050) ALUMINUM.
3. FINISH: BLACK ANODIZED
4. FUSE: DATA: 115 V AC - 1 AMP INST
   230 V AC - 0.5 AMP. INST.



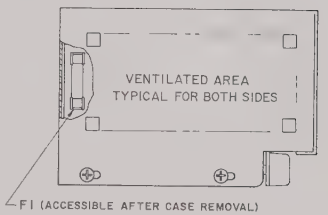


FIG. 1-2 MECHANICAL OUTLINE DRAWING.

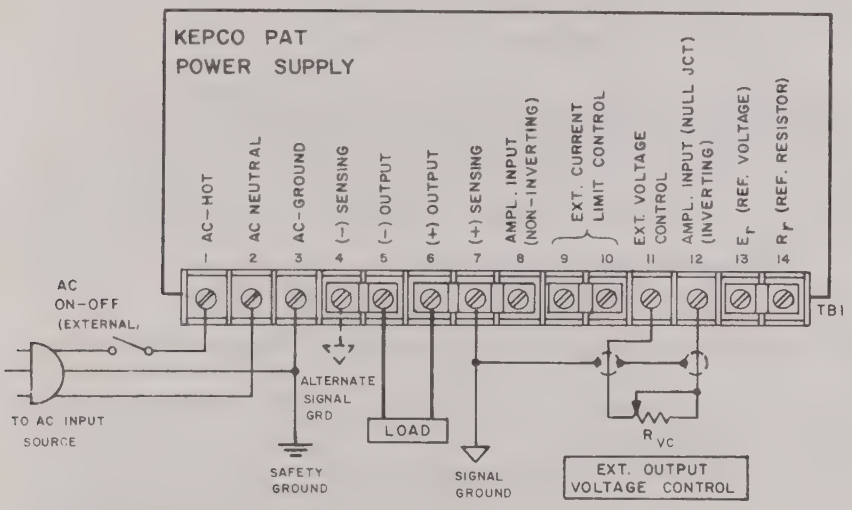
SECTION II – INSTALLATION

2-1 UNPACKING AND INSPECTION

2-2 This power supply has been inspected and tested at the factory prior to packing and is shipped ready for operation. After careful unpacking, inspect the instrument for shipping damage before attempting to operate it. Perform an operational check as described below. (See par. 2-9.) If there is any indication of damage, file a claim immediately with the responsible transport service.

2-3 TERMINATIONS

2-4 All input and output connections on the PAT power supplies are made on the 14-terminal barrier-strip as shown in FIG. 2-1.



NOTES:
1) The jumper links on TB1 must be securely connected as shown in the figure.
2) Connect load and external R_{VC} as shown before applying a-c input.

ATTENTION!
A FOLD–OUT DIAGRAM IS PROVIDED AT THE END OF SECTION III (FIG. 3-16), RELATING THE BARRIER-STRIP CONNECTIONS TO THE CIRCUITRY OF THE PAT POWER SUPPLY.

FIG. 2-1 TERMINATIONS AND STANDARD JUMPER-LINK CONNECTIONS.

2-5 A-C POWER REQUIREMENTS

2-6 This power supply is normally shipped for operation from a nominal 115V a-c source, single phase. If 230V a-c operation is desired, the transformer primary link connections must be changed as shown in FIG. 2-2.

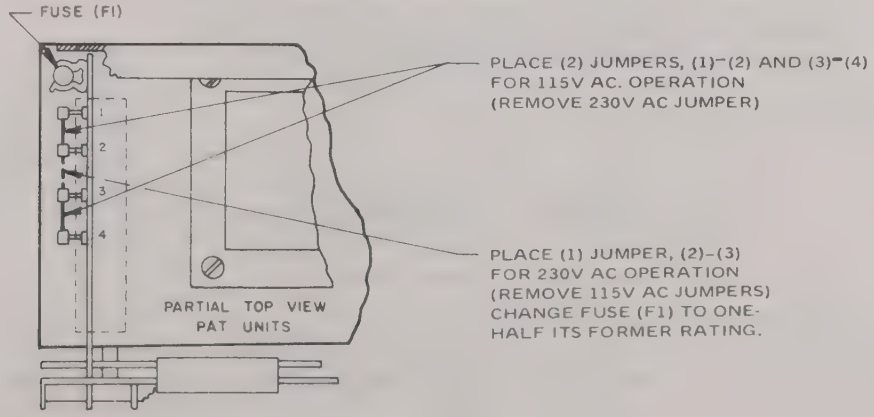

FUSE (F1)
PLACE (2) JUMPERS, (1)–(2) AND (3)–(4) FOR 115V AC. OPERATION (REMOVE 230V AC JUMPER)
PLACE (1) JUMPER, (2)–(3) FOR 230V AC OPERATION (REMOVE 115V AC JUMPERS) CHANGE FUSE (F1) TO ONE-HALF ITS FORMER RATING.
PARTIAL TOP VIEW PAT UNITS

FIG. 2-2 CONVERSION TO 230V A-C OPERATION.

2-7 GROUNDING

2-8 a) SAFETY GROUND. For safety reasons it is important that the metal chassis of the PAT power supply be connected to an a-c ground. If a 3-wire line cord and a properly grounded outlet are used, the grounding is automatically accomplished. If only a 2-wire line cord is used, the metal chassis of the power supply must be grounded separately by returning barrier-strip terminal (3) to an a-c ground.

b) SYSTEM GROUND. The d-c output terminals of the PAT power supplies are isolated from the a-c power line and from the chassis, except for a capacitor/resistor series connection from the negative (−) output to chassis. Since no d-c connection exists to the chassis, either side of the power supply may be grounded. A maximum of 500V (d-c or p-p) can be connected between chassis and either output terminal. If the provided internal capacitor/resistor connection from (−) output to chassis is not desired, and the positive side of the PAT is to be grounded, the system ground may be established outside the power supply by removing the provided flexible grounding lead from the grounding lug and transferring it to the "blind" or ungrounded position. (Refer to FIG. 2-3.) The output of the PAT is now completely "floating," and either the positive or negative may be grounded elsewhere in the system or at the load.

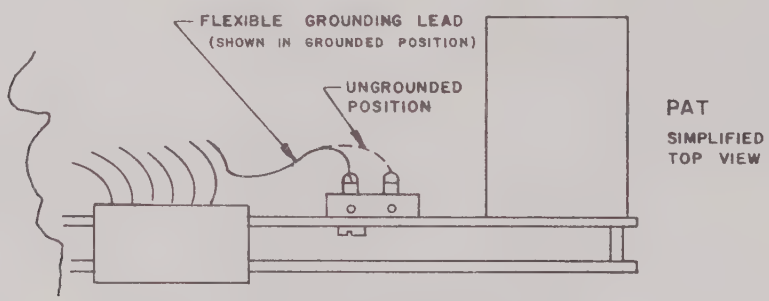
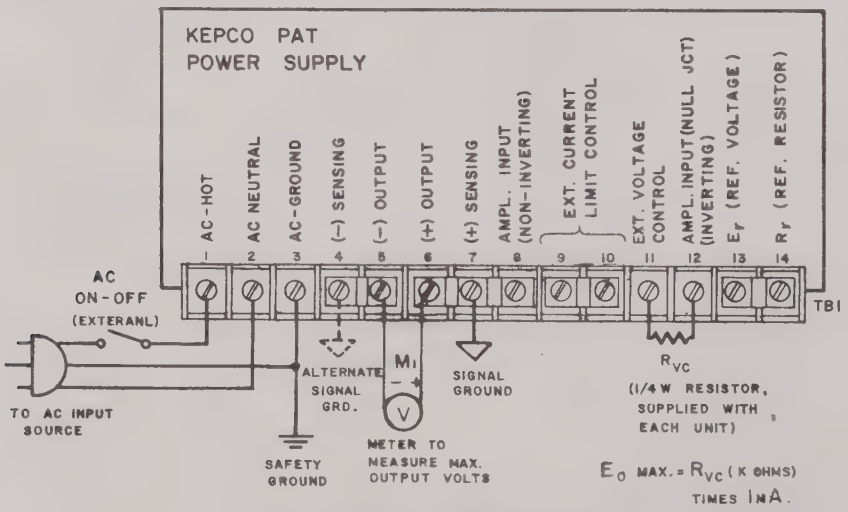


FIG. 2-3 LOCATION OF GROUNDING LUG.

2-9 OPERATIONAL CHECK

2-10 After a-c power connections have been made as described in paragraph 2-5, a functional check may be performed as follows:

Connect an external voltmeter (M1) to terminals (5)—(6) as shown in FIG. 2-4. Connect a line cord to terminals (1)—(2) with the ground wire to terminal (3).



NOTES:
1) The jumper links on TB1 must be securely connected as shown in the figure.
2) Connect load and external R_{VC} as shown before applying a-c input.

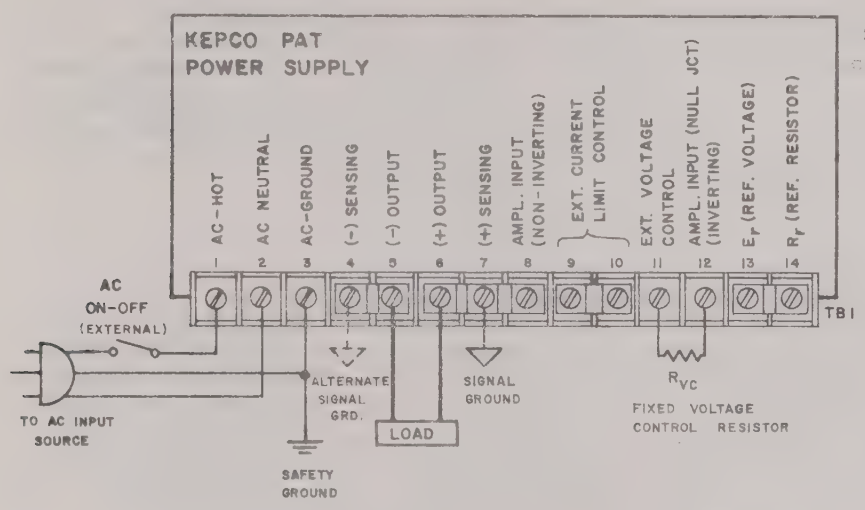
ATTENTION!
A FOLD—OUT DIAGRAM IS PROVIDED AT THE END OF SECTION III (FIG. 3-16), RELATING THE BARRIER--STRIP CONNECTIONS TO THE CIRCUITRY OF THE PAT POWER SUPPLY.

FIG. 2-4 OPERATIONAL CHECKOUT.

2-11 The resistor which is connected to terminals (11)—(12) serves as the voltage control resistor (R_{VC}). It is intended for preliminary checkout purposes only. ***Regulation and stability measurements require metal film or wirewound resistors with temperature coefficients of 20 ppm or better.*** The output voltage measured on M1 should be proportional to the resistance of R_{VC}. Since a nominal 1000 ohm per volt control ratio is used in the PAT power supplies, the output voltage will be approximately equal to 1 mA times the kilohms across terminals (11)—(12). A variable resistor of appropriate value may be used to check the supply over its full output range. This completes the operational test of the PAT power supply.

3-1 STANDARD POWER SUPPLY OPERATION FOR STABILIZED OUTPUT VOLTAGE

3-2 GENERAL. The PAT power supply is shipped from the factory with 5 removable jumper-links in place at the rear barrier-strip (TB1). All links must be in place and secured tightly for standard (local sensing) operation. With the external voltage control resistor (R_{VC}) mounted between terminals (11) and (12) on TB1, the power supply will deliver a minimum rated output voltage equal to R_{VC} (kilohms) X 1 mA).



NOTES:
1) The jumper links on TB1 must be securely connected as shown in the figure.
2) Connect load and external R_{VC} as shown before applying a-c input.

ATTENTION!
A FOLD–OUT DIAGRAM IS PROVIDED AT THE END OF SECTION III (FIG. 3-16), RELATING THE BARRIER--STRIP CONNECTIONS TO THE CIRCUITRY OF THE PAT POWER SUPPLY.

FIG. 3-1 CONNECTIONS FOR FIXED OUTPUT OPERATION.

3-3 LOAD CONNECTION. The load may be connected as shown in FIG. 3-1. Load connecting wires should be as heavy as practicable since load wire voltage drops will degrade regulation at the load. Specified power supply performance will in this case be present at the error-sensing terminals (4)–(7). Refer to Section V for correct connections when measuring power supply performance.

3-4 ERROR SENSING. Specified stabilization performance *directly at the load* requires the use of remote error sensing. Error sensing wires should be of the same gage as the load wires and each error sensing lead should be tightly twisted with its respective load wire. (See FIG. 3-2.) Observe polarities. The negative sensing lead from terminal (4) must go to the negative load side.

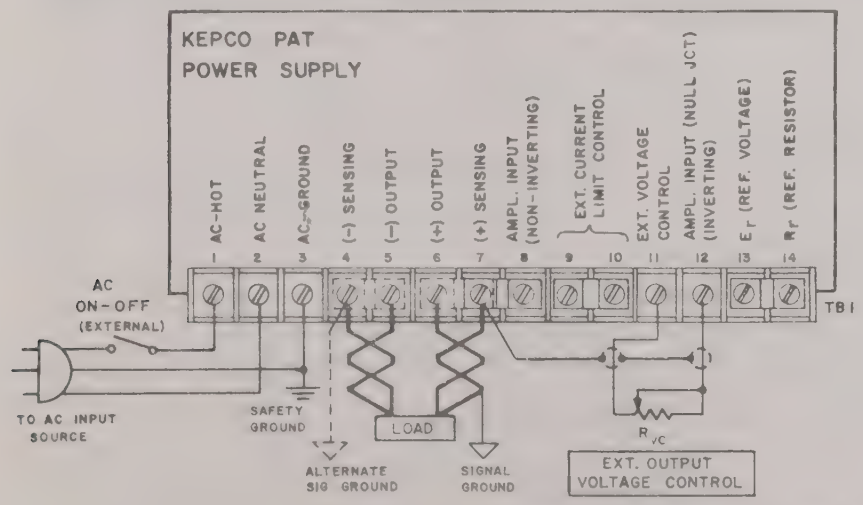
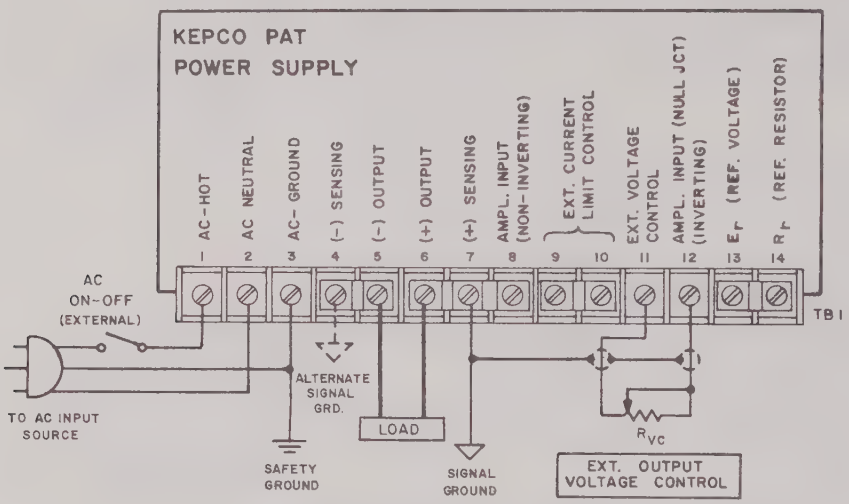


FIG. 3-2 LOAD CONNECTION WITH REMOTE ERROR SENSING.

3-5 TURN-ON. An a-c source switch may be mounted externally to facilitate connection to and from the power line. If only a single pole switch is used, it should be connected in series with terminal (1), the "hot" side of the power source.

3-6 VARIABLE OUTPUT VOLTAGE. The output voltage of the PAT power supply may be varied from approximately zero to its maximum rated value by replacing the supplied fixed voltage control resistor by a suitable rheostat. The control ratio (ohms per volt) is approximately 1000 ohms per volt in all PAT power supplies so that for each 1 kilohm of control resistance, 1 volt of output voltage will be available. A good quality, wirewound or metal film potentiometer (rheostat connected) should be used as the voltage control resistance. Shielding of the leads going to the voltage control is recommended to reduce ripple "pickup." (Refer to FIG. 3-3.)



NOTES:
1) The jumper links on TB1 must be securely connected as shown in the figure.
2) Connect load and external Rvc as shown before applying a-c input.

ATTENTION!
A FOLD-OUT DIAGRAM IS PROVIDED AT THE END OF SECTION III (FIG. 3-16), RELATING THE BARRIER-STRIP CONNECTIONS TO THE CIRCUITRY OF THE PAT POWER SUPPLY.

FIG. 3-3 CONNECTION FOR VARIABLE OUTPUT VOLTAGE.

3-7 INTRODUCTION TO PROGRAMMING

3-8 GENERAL. A few general remarks may be in order to familiarize the user of this equipment with the terminology and basic equations pertaining to remote programming of the Kepco PAT Power Supplies. Electrically, the power supply consists of the unregulated d-c source (E_u), the pass element (E_p), the d-c error amplifier (A), and a comparison circuit which resembles a 4-arm electrical bridge. (Refer to FIG. 3-4.) The elements of the bridge are arranged to produce a virtual zero at the amplifier input when the bridge circuit is at balance ($V_{AA'} = 0$). Any tendency for the output voltage to change in relation to the reference voltage (E_r) creates an error signal which, by means of negative feedback and the amplifier, tends to correct the output voltage towards restoration of bridge balance.

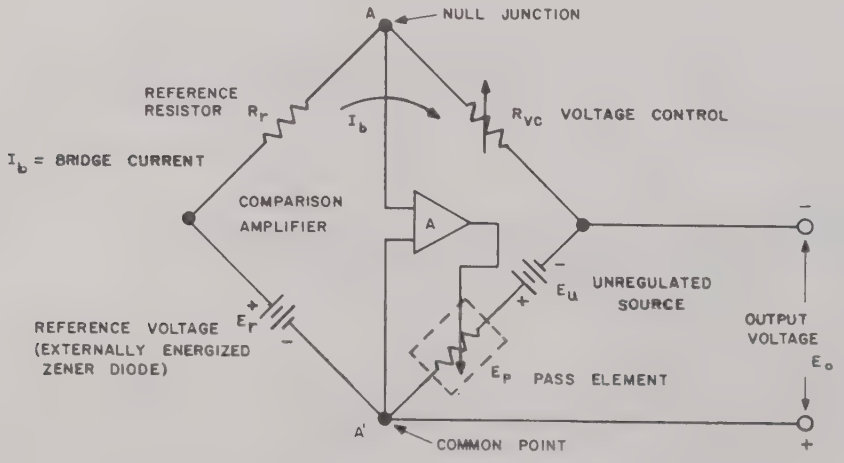


FIG. 3-4 KEPCO POWER SUPPLY AND COMPARISON BRIDGE CIRCUIT.

3-9 EQUATIONS SHOWING THE OPERATION OF THE KEPCO BRIDGE

3-10 The following relationships govern the operation of the Kepco Bridge at balance; i.e., with $V_{AA'} = 0$:

a) $$\frac{E_O}{E_r} = \frac{R_{VC}}{R_r} \quad (1)$$

$$\frac{E_r}{R_r} = I_b \quad (2)$$

$$E_O = I_b R_{VC} \quad (3)$$

Where. E_O = Output voltage
E_r = Reference voltage
R_r = Reference resistance
R_{VC} = Control resistance
I_b = Bridge current

b) The ratio of the number of ohms control resistance needed per volt output is termed the CONTROL RATIO. It is nominally 1000 ohm/volt in the PAT power supplies.

c) As can be seen from equation (1), the output voltage E_O can be controlled by varying any one of the three quantities. Rewriting equation (1) we have:

$$E_O = \frac{E_r}{R_r} \times R_{VC}$$

The ratio E_r/R_r constitutes the bridge current, I_b (Eq. 2).

d) Therefore we can write. $E_O = I_b R_{VC}$ (Eq. 3).

Making I_b a precision quantity (precision bridge current adjustment is described in par. 3-11) establishes a precise programming ratio so that the value of E_O is solely dependent upon R_{VC}. This mode of operation is referred to as RESISTANCE PROGRAMMING and is covered in detail in paragraph 3-16.

e) Rewriting equation (1), $E_O = E_r (R_{VC}/R_r)$, we can make E_r the variable which controls E_O. This type of control is referred to as VOLTAGE PROGRAMMING and is covered in paragraph 3-28.

f) Many other modes of control are, of course, possible; some of them are described in the following paragraphs. For a more extensive treatment and a detailed theoretical view of power supply applications, refer to various Kepco technical publications available from your Kepco representative or directly from Kepco Applications Engineering Department.

NOTE: For all programming and adjustment components, use high quality, wirewound resistors with a TC of 20 ppm or better.

3-11 ADJUSTMENTS FOR EXACT PROGRAMMING RATIO

3-12 Referring to equation (3), $E_O = I_b R_{VC}$, it is seen that if $I_b = 1$ mA, 1000 ohms of control resistance (R_{VC}) are needed for each volt of output. Making I_b therefore a precision quantity, the accuracy and linearity of the output voltage will then solely depend upon R_{VC}.

3-13 Again referring to equation (3), we see that if $R_{VC} = 0$ (shorted out), the output voltage should be zero. A small negative offset voltage (in the millivolt range) exists, however, under this condition at the output.

3-14 Both inaccuracies, the slightly larger bridge current (I_b) and the small negative offset voltage, may be adjusted to provide a linear and precise programming ratio.

3-15 PROCEDURE, PRECISION PROGRAMMING RATIO ADJUSTMENT (Refer to FIG. 3-5.)

a) Equipment required:

1) Precision digital or differential voltmeter (M1).

2) Precision resistor, accuracy comparable to M1. The value is not important but must be known. For every 1000 ohms, 1 volt will appear across M1 (R_{VC}).

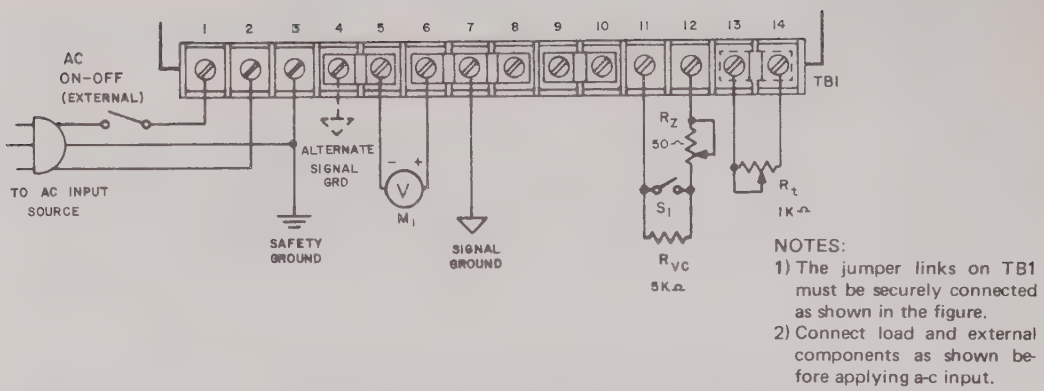


FIG. 3-5 CONNECTIONS FOR PRECISION PROGRAMMING ADJUSTMENTS.

3) Bridge current adjustment rheostat, wirewound or metal film, 1 K ohm—1/2 watt (R_t).

4) Zero adjustment rheostat, wirewound or metal film, 10 ohm—1/2 watt (R_Z).

5) Single pole, single throw switch (S1).

b) Connect calibration setup per FIG. 3-5, and connect the PAT power supply to the a-c power source.

NOTE: **Models with suffix "R" do not require the external resistor, R_t. These models contain an internal calibration adjustment (I_b Adj. — R16) to trim the bridge current to exactly 1 mA. The jumper link (13)—(14) on TB1 should be in place on models with suffix "R."**

Models with suffix "E" do not require the external resistor, R_Z. These models contain an internal calibration adjustment (E_{lo}null — R10) to zero the offset voltage. The external resistor (R_Z) is replaced on these models with a direct connection to terminal (12) on TB1.

c) With S1 "open," and R_{VC} = 5 K ohms, approximately 5 volts will be read out on M1. Adjust R_t (bridge current adjustment) or R16 in models with suffix "R," until ***exactly*** 5 volts are read out on M1.

d) Close S1 and note deviation from zero on M1 (approximately 2 to 8 mV negative). Adjust R_Z (zero adjustment) or R10 in models with suffix "E," until exactly zero volts are read out on M1.

e) Open S1 and check the 5 volt reading. Repeat "C" and "D" as necessary to achieve the desired accuracy. FIG. 3-6 below shows graphically how the programming ratio can be precision adjusted.

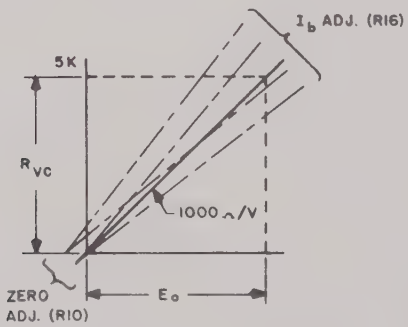


FIG. 3-6 ESTABLISHING PROGRAMMING LINEARITY.

3-16 PROGRAMMING OUTPUT VOLTAGE WITH RESISTANCE

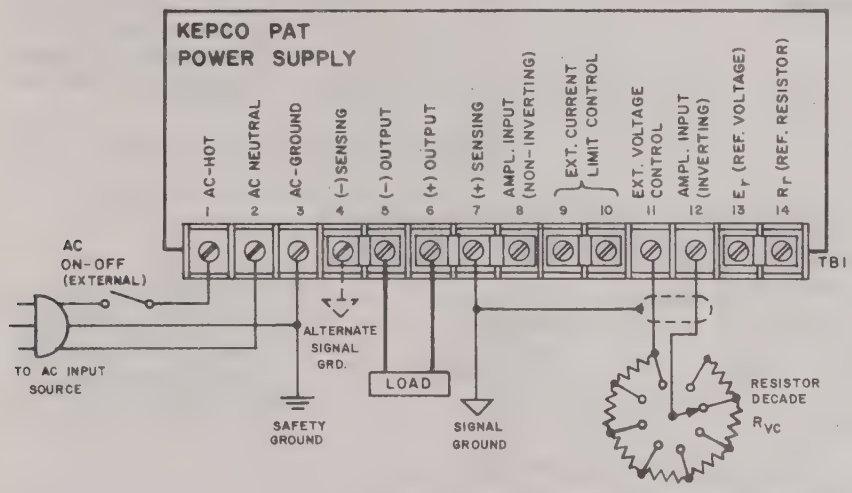
3-17 The output voltage of the PAT power supply can be varied by means of a control resistance connected between terminals (11) and (12) of the barrier-strip. This resistance may be fixed, continuously adjustable, or stepped, or it may be a combination of these. When fixed resistors are switched as the voltage control resistance, the switching device must have "make before break" contacts in order that the control circuit does not open during transfer. While the steady state current through R_{VC} is only 1 mA, it will rise to considerably higher peak values when larger voltage steps are programmed. The programming resistors must absorb these peak currents which consist of energy stored in the output capacitor. Consequently, it is recommended that the dissipation rating of these programming resistors is made several times the steady state power dissipated in them.

The value of the output voltage is given by equation (1)

$$E_o = R_{VC} \left(\frac{E_r}{R_r}\right)$$

Since $\frac{E_r}{R_r} = I_b$ (2), it follows that $E_o = I_b R_{VC}$ (3).

Referring to equation (3), we see that since I_b is 1 mA in the PAT supplies (and can be precisely adjusted as shown in par. 3-11), for every volt of output, 1000 ohms control resistance must be provided. This corresponds to a "programming ratio" of 1000 ohms per volts.



NOTES:
1) The jumper links on TB1 must be securely connected as shown in the figure.
2) Connect load and external R_{VC} as shown before applying a-c input.

ATTENTION!
A FOLD-OUT DIAGRAM IS PROVIDED AT THE END OF SECTION III (FIG. 3-16), RELATING THE BARRIER-STRIP CONNECTIONS TO THE CIRCUITRY OF THE PAT POWER SUPPLY.

FIG. 3-7 CONNECTION FOR RESISTANCE PROGRAMMING.

3-18 PROCEDURE (Refer to FIG. 3-7.)

a) Determine value of programming resistor(s) for output voltage desired.

b) Using 2-wire, shielded cable, connect the chosen resistors to terminals (11) and (12). Connect the shield to the system ground point.

c) The output voltage will vary from zero to (1 mA) X (R_{VC}) as R_{VC} is adjusted from zero to its maximum value.

3-19 CURRENT STABILIZED OPERATION

3-20 In the current stabilized operating mode, the voltage comparison bridge is interconnected with an external current sensing resistor (R_S) and a current control (R_{CC}) as shown in FIG. 3-8 to maintain a constant voltage drop across R_S. In this way, an adjustable stabilized load current is obtained. Characteristic of the stabilized current supply is the ability to change its output voltage automatically in order to maintain a stabilized current through a range of possible load resistances. The range of output voltage that the supply can deliver and simultaneously maintain stabilized current is referred to as the COMPLIANCE VOLTAGE.

3-21 The current sensing resistor R_S is chosen to develop a 1 volt drop at the maximum desired current. It is calculated by dividing this current into 1 volt. The value of R_S is not critical and can be the nearest standard resistance available. Several facts should be kept in mind, however, when choosing R_S. A compromise must be made between a large and a small value. While a large value is desirable for good current regulation, it is less so in view of the power dissipated across it. It must be remembered that all the load current is flowing through R_S and the input to the regulator is connected across it. It is therefore vital that all extraneous changes across R_S; i.e., resistance change due to temperature, are kept to a minimum. A high quality, low TC (20 ppm) resistor at least 10 times the actual wattage needed, is therefore strongly recommended.

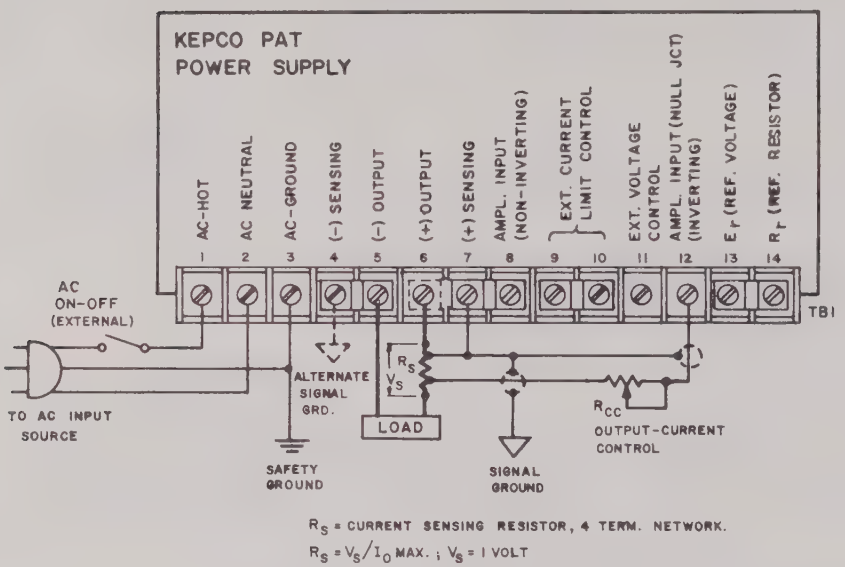
3-22 The current control resistor is chosen on the basis of the control ratio of the Kepco comparison bridge, and V_S, the maximum voltage across R_S. If R_S was selected for 1 volt drop, then V_S = 1 volt and $R_{CC} = V_S$ times the control ratio or:

$$R_{CC} = 1 \text{ volt } (1000 \text{ ohms/volt}) = 1000 \text{ ohms}$$

A high quality, low TC (20 ppm) resistor is recommended for R_{CC}.

3-23 PROCEDURE (Refer to FIG. 3-8.)

The actual setup procedure for current regulation is perhaps best shown by a practical example. A Model PAT 72–0.3 is to be set up for current stabilized output from 30 mA to 300 mA.



R_S = CURRENT SENSING RESISTOR, 4 TERM. NETWORK.
$R_S = V_S / I_{O\,MAX.}$; V_S = 1 VOLT

NOTES:
1) The jumper links on TB1 must be securely connected as shown in the figure.
2) Connect load and external R_{CC} as shown before applying a-c input.

ATTENTION!
A FOLD–OUT DIAGRAM IS PROVIDED AT THE END OF SECTION III (FIG. 3-16), RELATING THE BARRIER-STRIP CONNECTIONS TO THE CIRCUITRY OF THE PAT POWER SUPPLY.

FIG. 3-8 CONNECTIONS FOR STABILIZED CURRENT OPERATION.

a) R_S is chosen by:

$$R_S = \frac{1 \text{ volt}}{0.3A} = 3.33 \text{ ohms.}$$

$$P_{diss} = V_S I_{max.} = 1 \text{ volt } (0.3A) = 0.3 \text{ watts.}$$

Three 10 ohm, 1 W, wirewound resistors connected in parallel will be satisfactory.

b) The control resistance R_{CC} is found by calculating its limits:

$$R_{CC} \text{ (max.)} = V_S \text{ (max.) X control ratio} = 1V \text{ X } \frac{1000 \text{ ohms}}{V} = 1000 \text{ ohms.}$$

$$R_{CC} \text{ (min.)} = V_S \text{ (min.) X control ratio} = 0.1V \text{ X } \frac{1000 \text{ ohms}}{V} = 100 \text{ ohms.}$$

As R_{CC} is therefore varied from 100 ohms to 1000 ohms, the regulated current will vary from 30 to 300 mA, and the compliance voltage from approximately 0 to 71 volts.

c) Since, in the above example, the sensing voltage across R_S went below 1 volt at the low current output, the current regulation may not be satisfactory for the purpose intended. (Specifications are based on 1 volt sample.) To keep output effects within specifications, we must calculate R_S under minimum current conditions.

V_S min. = 1 volt

$I_{min.}$ = 0.03A

$$R_S = \frac{1 \text{ volt}}{0.03A} = 33.3 \text{ ohms}$$

The actual power dissipation of R_S is given by

$$P_{diss} = V_S \text{ (max.)} \times I_O \text{ (max.)} = 10V \times 0.3A = 3 \text{ watts}$$

(Three 100 ohm, 10 W, wirewound resistors connected in parallel will be satisfactory.)

R_{CC} will now be

$$R_{CC} \text{ (max.)} = V_S \text{ (max.)} \times \text{control ratio} = 10V \times 1000 \text{ ohm/V} = 10 \text{ K ohms.}$$

$$R_{CC} \text{ (min.)} = V_S \text{ (min.)} \times \text{control ratio} = 1V \times 1000 \text{ ohm/V} = 1 \text{ K ohm.}$$

d) In general, a new value for R_S should be calculated if the required output current range exceeds a 1:10 ratio. Below this range, performance will be adequate for most applications.

3-24 PROGRAMMING BY CONDUCTANCE

3-25 For special applications, the output voltage E_O can be adjusted by varying the bridge current. The relationship governing this type of programming is:

$$E_O = E_r (R_{VC}) (G), \qquad \text{Where: } G = \frac{1}{R_r + R_X} = \text{programming conductance, or,}$$

$$E_O = \frac{E_r}{R_r + R_X} (R_{VC})$$

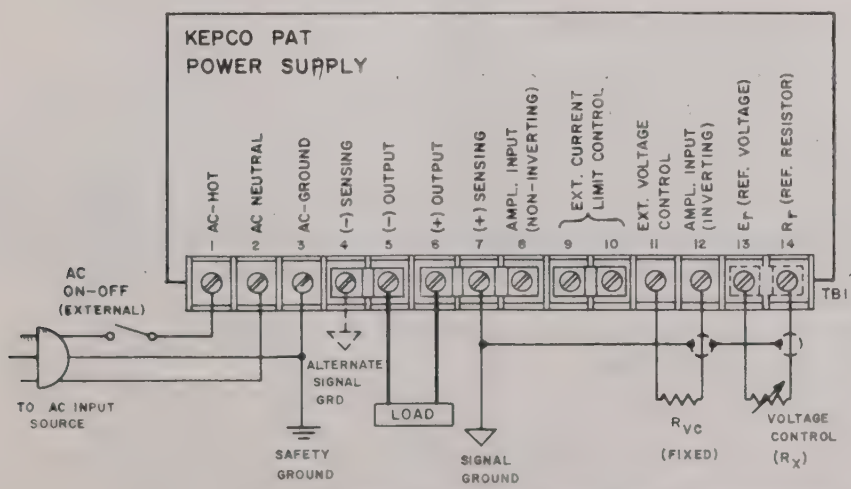
E_O = Output voltage
E_r = Reference voltage
R_{VC} = Control resistance
R_r = Reference resistance
R_X = Programming resistor

Since $E_r/R_r = I_b$ and $E_O = I_b R_{VC}$, the output voltage varies directly as I_b changes. Changing I_b with the help of an additional resistor in series with R_r results in an inversely proportional change of I_b since now:

$$I_b = \frac{E_r}{R_r + R_X}$$

This method of output voltage adjustment is therefore referred to as *Conductance Programming.*

3-26 Conductance programming is a linear, but reciprocal function when analyzed in terms of resistance. It can, however, be very useful, especially over a limited range and for small changes in output voltage. Another distinct advantage of this type of programming is the "built-in" safety feature. Should the programming circuit open accidentally, the programming resistance becomes infinite, the conductance is zero, and consequently, the output voltage becomes zero.



NOTES:
1) The jumper links on TB1 must be securely connected as shown in the figure.
2) Connect load and external R_{VC} as shown before applying a-c input.

ATTENTION!
A FOLD-OUT DIAGRAM IS PROVIDED AT THE END OF SECTION III (FIG. 3-16), RELATING THE BARRIER-STRIP CONNECTIONS TO THE CIRCUITRY OF THE PAT POWER SUPPLY.

FIG. 3-9 CONNECTIONS FOR CONDUCTANCE PROGRAMMING.

3-27 PROCEDURE (Refer to FIG. 3-9.)

Example: E_o desired = 3 to 5 volts.

a) Select R_{vc} for maximum desired output voltage E_o.

$$R_{vc} = \frac{E_o}{I_b}, \quad R_{vc} = \frac{5V}{1\ mA} = 5\ K\ ohms$$

b) A change of E_o to 3 volts requires a bridge current reduction of:

$$\Delta I_b = \frac{\Delta E_o}{R_{vc}} = \frac{2\ volts}{5\ K\ ohms} = 0.4\ mA$$

c) Since the standard bridge current is:

$$I_b = \frac{E_r}{R_r} = 1\ mA,$$

the additional resistance needed for the new bridge current "I_b'" = 0.6 mA is:

$$I_b'(R_r + R_x) = E_r, \ R_x = \frac{E_r}{I_o} - R_r \text{ or } R_x = \frac{6\ volts}{0.6\ mA} - 6\ K = 4\ K\ ohms.$$

A 4 K ohm, WW, low TC potentiometer connected in series with the internal R_r (see FIG. 3-9) will change the output voltage from 3 to 5 volts when varied from 0 to 4 K ohms.

NOTE: A word of caution may be in order in regard to the changing of the bridge current. The 1 mA value has been selected for maximum stability in the zener reference circuit. A large departure from this value is not advisable. If a ±50% change in I_b is not sufficient to achieve the desired voltage swing, an external reference supply can be used.

3-28 PROGRAMMING OUTPUT VOLTAGE WITH EXTERNAL VOLTAGE

3-29 The output voltage (E_o) of the PAT power supply can be varied by means of another external voltage source. This external programming voltage supplies the control current (I_b) formerly delivered by the internal reference voltage of the PAT supply. The output voltage (E_o) "follows" the programming source and maintains a value that remains in a constant proportion to it. The relationship governing this type of programming can be expressed by the equation:

$$E_o = E_i \frac{R_{vc}}{R_i}$$

Where: E_o = Output voltage to be programmed.
E_i = Programming voltage (input).
R_{vc} = Voltage control resistance of the supply to be programmed.
(R_{vc} can be either internally or externally connected.)
R_i = External programming resistor (coupling resistor).

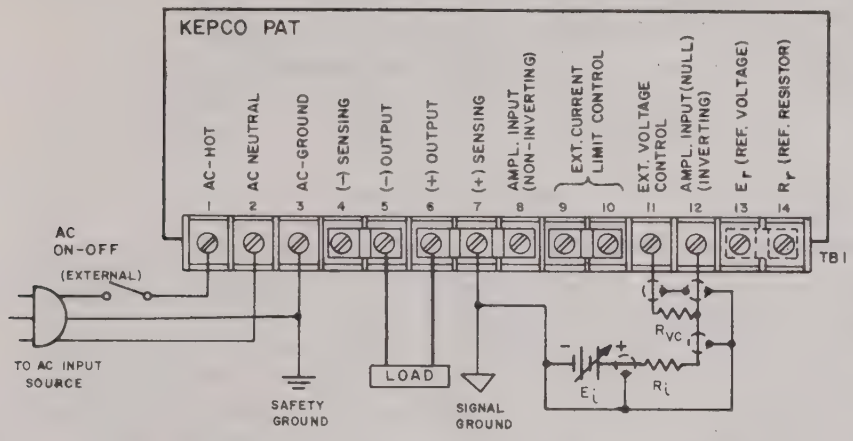
3-30 The values of the above quantities can be chosen to suit the particular application, although a few basic facts must be considered concerning their selection. E_o is, of course, limited by the maximum capability of the supply to be programmed. E_i can be any variable unipolar source having parameters at least as good as the desired E_o and being able to deliver the necessary I_b. The resistors R_i and R_{vc} are selected by first determining I_b, the desired bridge current.

Since I_b is supplied by E_i and also subtracts from the load current of the supply to be programmed, it should be kept as low as possible but not under 100 μA.

3-31 Once I_b is selected and E_i as well as the desired output voltage E_o are known, R_{vc} and R_i are calculated by these simple relations

$$R_{vc} = \frac{E_o}{I_b} \text{ (Eq. 2) where } I_b = \frac{E_i}{R_i} \text{ (4) and } E_o = \frac{E_i}{R_i} \times (R_{vc})$$

Stability, more than accuracy is the dominant criteria for selecting R_{vc} and R_i, since part of the resistance can be made adjustable to compensate for tolerances and offset voltages. Another important consideration in selecting control and programming resistors is to leave a wide safety margin when determining their wattage rating and to use only low temperature coefficient units.



NOTES:
1) The jumper links on TB1 must be securely connected as shown in the figure.
2) Connect load and external R_{VC} as shown before applying a-c input.

ATTENTION!
A FOLD–OUT DIAGRAM IS PROVIDED AT THE END OF SECTION III (FIG. 3-16), RELATING THE BARRIER--STRIP CONNECTIONS TO THE CIRCUITRY OF THE PAT POWER SUPPLY.

FIG. 3-10 CONNECTIONS FOR VOLTAGE PROGRAMMING.

3-32 PROCEDURE (Refer to FIG. 3-10.)

a) Connect components as shown in FIG. 3-10. Disconnect link between terminals (13) and (14) on the barrier-strip. Use shielded wire as indicated and connect shield to single system ground.

b) Component selection may be shown best in the form of an example.

Example

A Model PAT 40–0.5 is to be programmed from 0 to 30V.

The programming source available is a variable 10 volt source, capable of delivering 5 mA.

The bridge current (I_b) is selected to be 5 mA.

We find R_{VC} and R_i by using equations (3) and (4)

$$(3)\ R_{VC} = \frac{E_o}{I_b} = \frac{30V}{5\ mA} = 6\ K \quad (4)\ R_i = \frac{E_i}{I_b} = \frac{10V}{5\ mA} = 2\ K$$

Power dissipated in R_{VC} = 30V (0.005) = 0.15 W (use 1 W).

Power dissipated in R_i = 10V (0.005) = 0.05 W (use 0.5 W).

3-33 SERIES OPERATION OF PAT MODULES

3-34 GENERAL. Kepco PAT Modules can be series-connected for increased voltage output, provided the specified limits on voltage to chassis are not exceeded. When series-connected, the supplies should be protected by means of a semiconductor diode across the output terminals of each power supply, as shown in FIG. 3-11. The peak inverse rating of these diodes must be at least as large as the output voltage of the supply to which they are connected. The continuous current rating of the diodes should be at least as great as the largest short-circuit current of the interconnected supplies.

3-35 PROCEDURE

a) Connect load as shown in FIG. 3-11. Keep voltage drop in load wires as low as practical by using heavy gauge wires.

b) Connect protective diodes across respective output terminals.

c) Remove jumpers as shown and connect error-sensing leads. Refer to par. 3-4 for a description of the error sensing technique

d) Turn supplies "on" and adjust voltage on either control as required

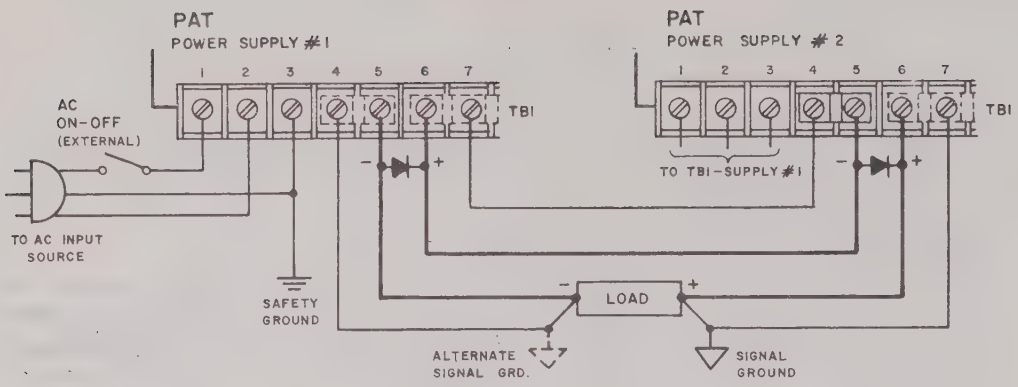


FIG. 3-11 SERIES CONNECTION OF PAT SUPPLIES.

3-36 PARALLEL OPERATION OF PAT MODULES

3-37 Two identical PAT power supplies may be operated in parallel for increased load-current output. Each supply is set approximately to the desired output voltage, with its respective current limiting adjustment at the factory-adjusted value (105% $I_{o\ max.}$). After paralleling the two power supplies, one of them (supply #1) will inherently be at a slightly higher output voltage than the other (supply #2). Consequently, supply #1 will deliver all the load current up the setting of its current limiting adjustment. As the load is increased beyond this limit of supply #1, supply #2 takes over and delivers the additional current. The current limiting adjustment of supply #1 can now be decreased so that approximately equal current sharing is obtained. FIG. 3-12 shows in form of a diagram how the two supplies operate in parallel with their respective current adjustments at the factory-adjusted value (105% $I_{o\ max.}$). It will be obvious from the diagram that the areas of load regulation are within the output current bands of the individual supplies only. Therefore, output effects due to load changes cannot be measured from zero to twice the load current, for example, but only within the individual load current bands. Error sensing as described in paragraph 3-4 (from either supply) may be used if precise regulation at the load is required.

NOTE: When paralleling power supplies, care should be exercised to avoid turning the voltage control of ***only one*** supply close to zero. This precaution is necessary to prevent possibly damaging currents in the voltage control resistor as its limiting resistance is lowered.

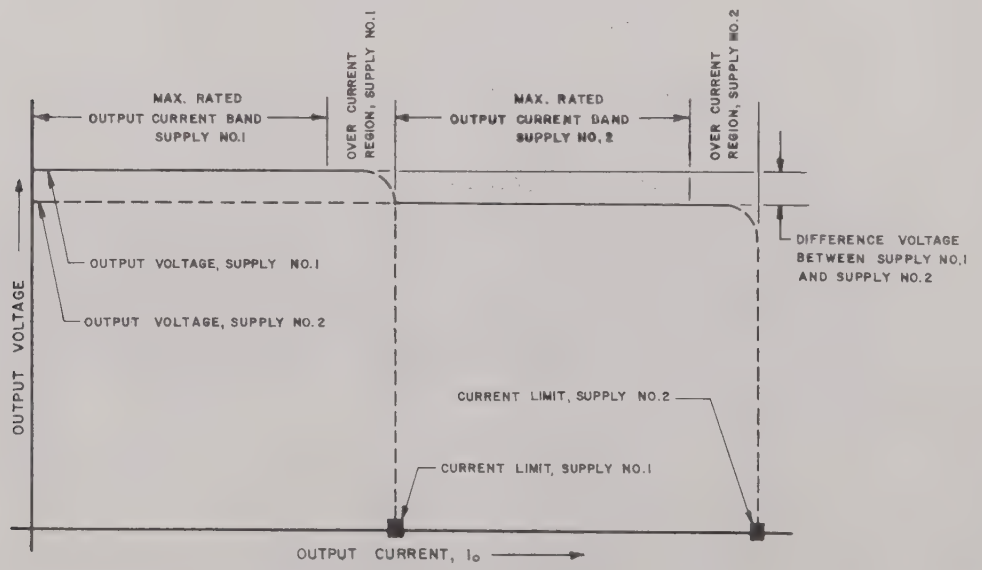


FIG. 3-12 PARALLEL OPERATION, PAT SUPPLIES.

3-38 PROCEDURE (Refer to FIG. 3-13.)

a) Connect units as shown in FIG. 3-13. Open SW-1 and SW-2 and connect to line

b) Close SW-1 and adjust both units to the approximate output voltage desired.

c) Close SW-2. Observe load current meters M1 and M2. Adjust Current Limit Adjust potentiometer R23 on the unit showing the higher current on its load current meter. Turn R23 counterclockwise until currents on M1 and M2 are approximately equal.

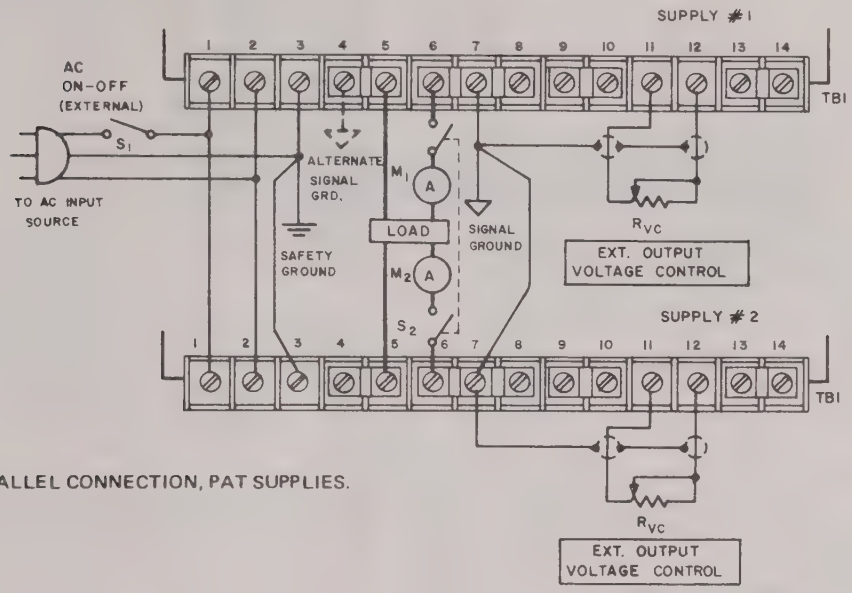


FIG. 3-13 PARALLEL CONNECTION, PAT SUPPLIES.

3-39 VOLTAGE PROGRAMMING WITH NONINVERTING INPUT

3-40 A useful property of a noninverting amplifier is the large input impedance developed by negative feedback. Noninverting input programming may therefore be used for applications where the source impedance is high and large control currents cannot be drawn from the programming source. Since the high input impedance of the noninverting configuration does not depend upon the magnitude of the feedback components, the latter may be selected quite low in a region of values where high quality, stable components are readily available. Their lower limit is, in fact, only given by the output capability of the operational power supply/amplifier. The common mode voltage limit must be observed for the amplifier used in the PAT power supplies (typically 8 volts) since excessive input voltages will saturate the amplifier. The approximate transfer function for noninverting operation may be expressed by:

$$E_O = E_i\left(\frac{R_{VC}}{R_i} + 1\right)$$

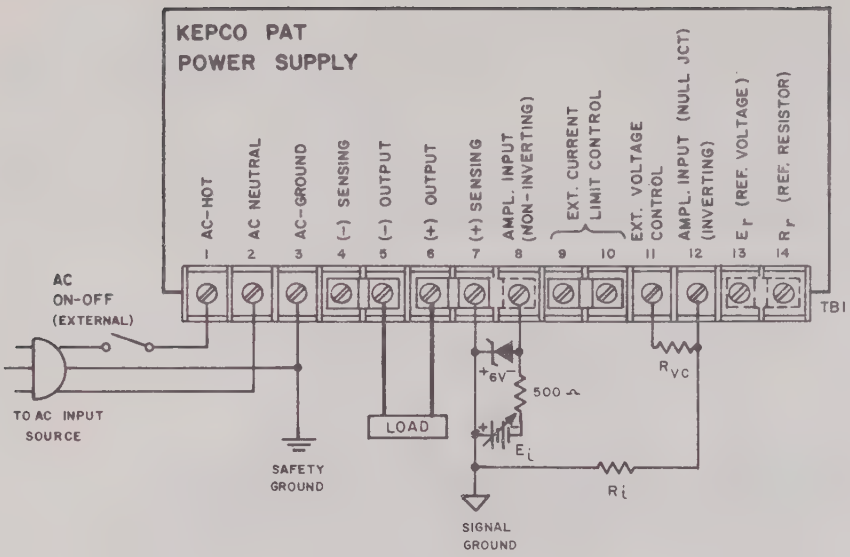
For exact equations, limitations and compensation circuits, please refer to the standard literature for operational amplifiers. A simple example illustrating the use of the noninverting input is given below.

3-41 A Model PAT 40–0.5 is available to produce a 2:1 replica of a -5V reference source. Referring to the transfer function given above, we must have: $E_O = 2\,E_i$; i.e., the ratio R_{VC}/R_i must equal unity or $R_{VC} = R_i$. If two 1 K ohm wirewound resistors are available, the output current loss of the PAT power supply due to the feedback current is negligible:

$$I_{feedback} = \frac{E_O}{R_{VC} + R_i} = \frac{10V}{1\,K + 1\,K} = 5\ mA.$$

No appreciable current is drawn from the reference source (except leakage current in the nanoampere range), but full load current (500–5 mA) can be drawn from the PAT 40–0.5 at twice the reference potential (2 X 5V = 10V).

An operational simplified schematic and the appropriate rear barrier-strip connections are shown in FIG. 3-14.



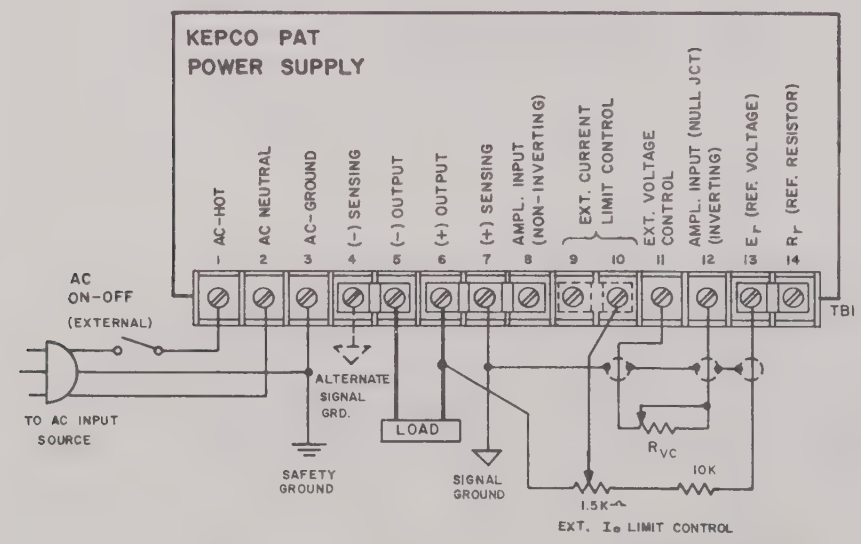
NOTES:
1) The jumper links on TB1 must be securely connected as shown in the figure.
2) Connect load and external R_{VC} as shown before applying a-c input.

ATTENTION!
A FOLD–OUT DIAGRAM IS PROVIDED AT THE END OF SECTION III (FIG. 3-16), RELATING THE BARRIER-STRIP CONNECTIONS TO THE CIRCUITRY OF THE PAT POWER SUPPLY.

FIG. 3-14 CONNECTIONS FOR VOLTAGE PROGRAMMING WITH NONINVERTING INPUT.

3-43 REMOTE CURRENT LIMITING

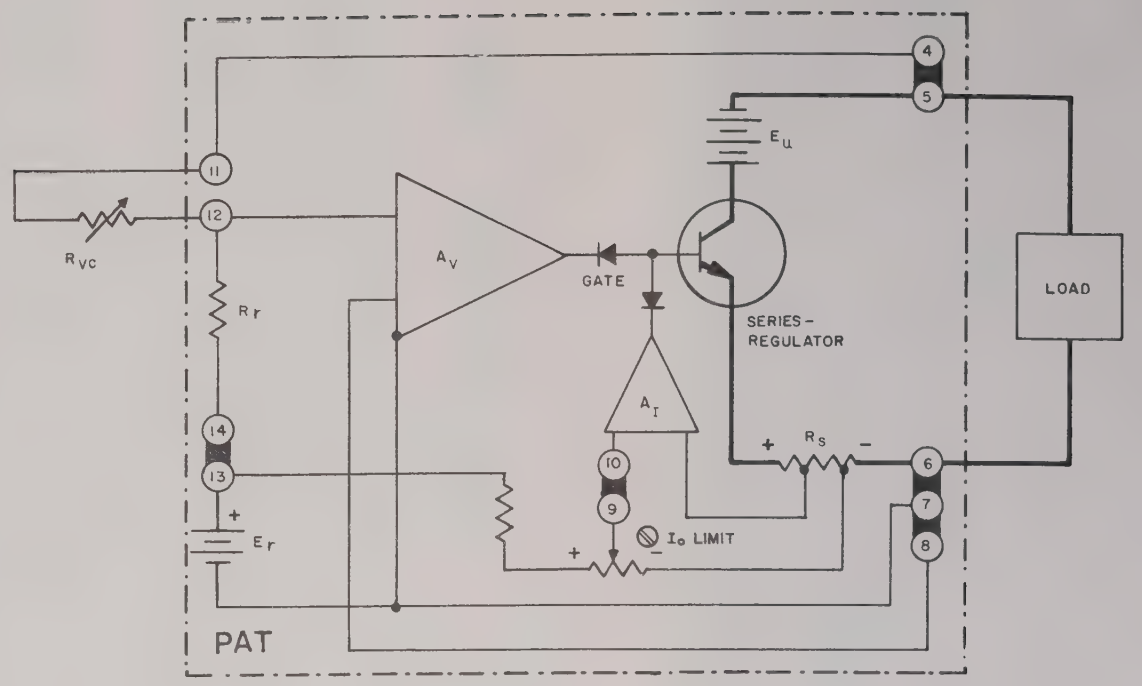
3-44 Barrier-strip terminals with a removable link [terminals (9)–(10)] have been provided on all PAT models, permitting the disabling of the internal limiting potentiometer and the connection of a remotely located, external control. Remote current limiting may be performed by substituting the internal component values and connecting them as shown in FIG. 3-15 below.



NOTES:
1) The jumper links on TB1 must be securely connected as shown in the figure.
2) Connect load and external R_{VC} as shown before applying a-c input.

ATTENTION!
A FOLD–OUT DIAGRAM IS PROVIDED AT THE END OF SECTION III (FIG. 3-16), RELATING THE BARRIER-STRIP CONNECTIONS TO THE CIRCUITRY OF THE PAT POWER SUPPLY.

FIG. 3-15 CONNECTIONS FOR REMOTE CURRENT LIMIT CONTROL.



NOTES:

1) Numbers refer to the barrier-strip terminals on TB1.
2) Diagram shows PAT connected for standard operation without sensing at the load and the output controlled by external resistor (R_{VC}).
3) Remove jumper links (4-5) and (6-7) for remote sensing.

FIG. 3-16 SIMPLIFIED CONNECTION DIAGRAM, KEPCO PAT MODELS.
(USE IN CONJUNCTION WITH BARRIER-STRIP DIAGRAMS IN SECTION III.)

SECTION IV – THEORY OF OPERATION

(References in CAPITAL LETTERS refer to the nomenclature used in FIG. 4-1.)

4-1 SIMPLIFIED DIAGRAM DISCUSSION. (Refer to FIG. 4-1.)

4-2 The POWER TRANSFORMER converts the a-c source voltage to the required levels needed to produce the d-c operating voltages for the MAIN and AUXILIARY SUPPLIES. The MAIN POWER SUPPLY is in series with the PASS ELEMENTS and the CURRENT SENSING RESISTOR and produces the output voltage. The MAIN PASS ELEMENTS are controlling their series voltage drop in a way tending to keep the OUTPUT VOLTAGE constant. The necessary drive for this change is produced by the DRIVER CIRCUIT which in turn receives its command signals either from the CURRENT LIMIT AMPLIFIER or the ERROR SIGNAL AMPLIFIER.

4-3 The OUTPUT VOLTAGE is constantly compared to the REFERENCE SOURCE while the output current is monitored by the CURRENT SENSING RESISTOR. Any change in either output voltage or current is amplified by the associated amplifier and transferred to the GATE DIODES which feed directly into the DRIVER CIRCUIT which produces the needed drive signal for the MAIN PASS ELEMENTS thus either stabilizing the OUTPUT VOLTAGE, or, if the signal from the CURRENT LIMIT AMPLIFIER is dominant, limiting the output current to the preadjusted value.

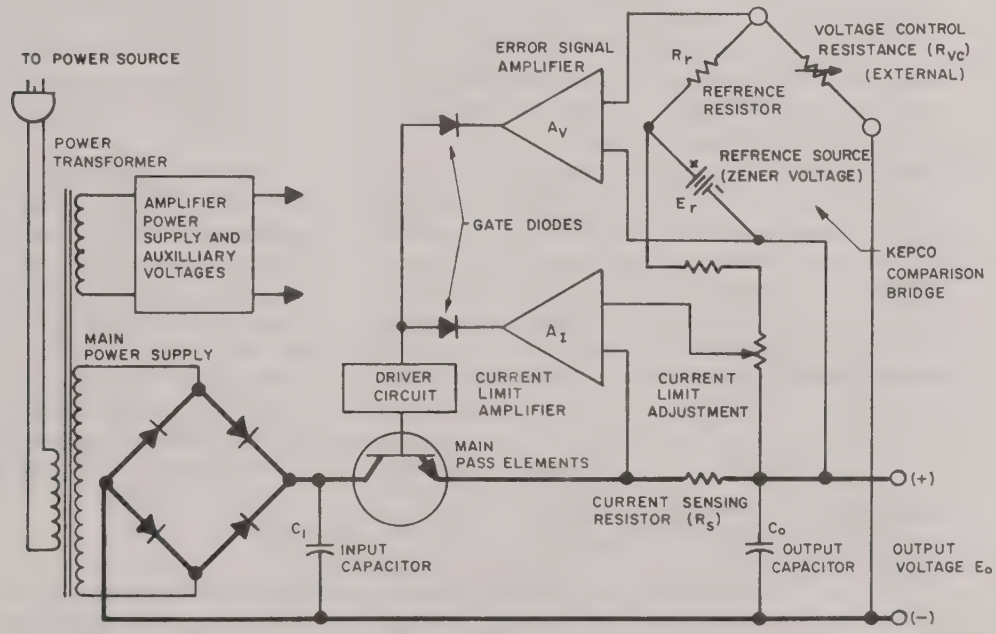


FIG. 4-1 SIMPLIFIED DIAGRAM, PAT POWER SUPPLIES.

4-4 CIRCUIT DESCRIPTION (Refer to Main Schematic, FIG. 6-1.)

4-5 For the purpose of analysis, the circuitry of the PAT power supply may be divided into several sections which are subsequently described below. Refer to the main schematic diagram, FIG. 6-3.

a) A-C INPUT CIRCUIT. A-C input power is introduced through rear barrier-strip TB1, terminals (1) and (2). Terminal (3) is connected to the chassis and should always be returned to a-c ground. The fuse (F1) protects the primary of the power ttransformer (T201) from excessive current. T201 has two primary windings which are connected either in parallel (for 115V a-c service) or in series (for 230V a-c service).

b) MAIN D-C POWER SUPPLY. The main d-c power is derived from a secondary winding on T201. Rectification and capacitive filtering are performed with silicon diodes CR15 through CR18 and C201. The main d-c power supply delivers the output current and the operating voltages for the series pass elements.

c) PASS ELEMENT CIRCUITRY. The pass element (or series control transistor, Q201 - Q202) is connected in series with the plus (+) output terminal, the external load, and the unregulated raw d-c voltage from the main power supply. By changing the conductance of the pass element, the voltage drop across it is changed in such a way as to stabilize the output voltage across the external load. The base drive current needed to effect the pass element voltage drop is supplied by the driver stage (Q4) which in turn is driven by either the error signal amplifier (A_v) or the current limit amplifier (Q2, Q3). PAT power supplies above 0.5A output current have two pass transistors in parallel (Q201 and Q202).

d) THE ERROR SIGNAL AMPLIFIER (A_v). This device is a d-c coupled, high-gain operational amplifier (plug-in). Its main function is to amplify the error signal derived from the comparison bridge to a level suitable to pass the gate circuit and drive Q4. Its inputs are brought out at the rear barrier-strip for programming the power supply output with input signals of either polarity.

e) THE COMPARISON BRIDGE. This 4-arm electrical bridge circuit is the regulating and controlling element in many Kepco Power Supplies. In the PAT models it consists of the reference voltage E_r (CR8), the reference resistor R_r (R15 and the optional bridge current adjustment rheostat R16), the voltage control resistance R_{VC} [either the optional internal control R13 or the resistor externally applied between terminals (11)—(12) on TB1], and the output voltage E_o. As shown in FIG. 4-1, the internal reference voltage E_r in series with the reference resistance R_r is continuously compared with the output voltage E_o in series with the voltage control resistance R_{VC}. At balance ($E_{AA'} = 0$), a constant bridge current I_b flows through the bridge, keeping the error signal at bridge terminals A—A' at approximately zero volts. Any deviation in E_o caused by a-c source, load variations, or other influences will tend to change I_b in the bridge circuit and thereby produce an error signal at the bridge terminals A—A'. This error signal is then amplified by the error signal amplifier A_v and acts as a control signal for the series regulator or pass element changing the voltage drop across it in such a way as to restore the previous output voltage level.

f) THE CURRENT LIMIT CIRCUIT. The differential inputs of the current limit amplifier (Q2, Q3) are connected to the current sensing resistor R_s (R26) and to a reference voltage. The voltage drop across R_s (due to load current flow) is continously compared to the preset voltage at the current limit control (R19). As long as the voltage developed across R_s is less than the preset voltage level at R19, the current amplifier will be biased to its inactive state and will have no effect on the output. Both the current limit amplifier and error signal amplifier (A_v) are coupled through their respective gate diodes (CR9, CR10) to the driver transistor (Q4). The amplifier with the larger (negative) output will control the pass elements while the other amplifier is decoupled. Since the voltage drop across the sensing resistor (R26) is proportional to the output current, and the current limit control (R19) may be set to a voltage proportional to the desired output current, the current amplifier is inactive as long as the output current does not exceed the preset level. If the output current does increase, however, the current amplifier will produce an output signal more negative than A_v, thereby taking control away from the voltage amplifier A_v and limiting the output current sharply in the process.

g) AUXILIARY SUPPLIES

AMPLIFIER POWER SUPPLY. This full-wave, capacitor-filtered source consists of a center-tapped winding on T201, silicon rectifiers CR1, CR2, and the filter capacitor C1. A current regulator stage (Q1) followed by a string of zener diodes (CR4, CR5, CR6, CR7) provides the operating voltages for the amplifiers and the comparison bridge circuit.

h) DRIVER COLLECTOR SUPPLY. A half-wave, capacitor-filtered supply is derived from a winding on T201, rectified by silicon diode CR13 and filtered by C6. It delivers collector voltage to the driver transistor (Q4). R17 is a bleeder resistor, and CR14 is used to prevent latch-up under transient conditions.

i) I_{cbo} SUPPLY. The operating voltages for the I_{cbo} circuitry are derived from the main power supply. Q5 is biased by the voltage drop across the series diodes (CR19, CR20) in such a way as to draw a relatively constant current, thus providing a current sink for the I_{cbo} of the pass elements. This ensures positive cutoff, especially at high operating temperatures.

SECTION V – MAINTENANCE

5-1 GENERAL

5-2 This section covers maintenance procedures, installation of optional components, calibration, and test measurements of the Kepco Model PAT Power Supplies. Conservative rating of components and the noncongested layout should keep maintenance problems to a minimum. If trouble does develop, however, the easily removed wrap-around cover and the plug-in feature of the circuit boards provide exceptional accessibility to all components of the supply.

5-3 DISASSEMBLY (Refer to FIG. 5-1.)

5-4 The wrap-around cover may be easily removed by loosening the (5) holding screws (2 on each side and one on top). For component replacement, the printed circuit board assembly may be removed by loosening its (4) mounting screws and disconnecting the (2) mating connectors from the PCB assembly.

5-5 INTERNAL ADJUSTMENT PROCEDURES

5-6 CURRENT LIMITING CONTROL ADJUSTMENT (Refer to FIG. 5-2) A single-turn potentiometer (R19) conveniently located on the topside of the printed circuit board serves as an adjustment for limiting the output current through the range of 5% to 105% of its maximum rated value. It is factory-adjusted for approximately 5% above the maximum rated output current and has to be reset only if a component should have to be replaced or if a lower limit than the preset value is desired.

A convenient way to make the adjustment is as follows:

a) Set up supply to draw the maximum desired output current and monitor the output with an oscilloscope having a vertical sensitivity of at least 0.1 mV/cm.

b) If the desired current cannot be drawn, turn the current limit adjustment R19 slowly clockwise until the current, as read on an accurate ampere meter in series with the load, reaches the desired value. Continue the clockwise adjustment approximately 10° beyond this point.

c) Observe ripple. Decrease the current limiting by slowing rotating R19 counterclockwise until output ripple increases substantially. Back off on R19 until output ripple appears normal again. Short circuit the power supply by connecting the d-c ampere meter across its output terminals. The short-circuit current will be approximately 5% of the maximum rated current above the set limit.

NOTE: A too-close setting of the current limit adjustment may result in premature limiting of the output current. It is therefore advisable to let the unit reach thermal equilibrium at its expected ambient temperature environment before the final adjustment is made.

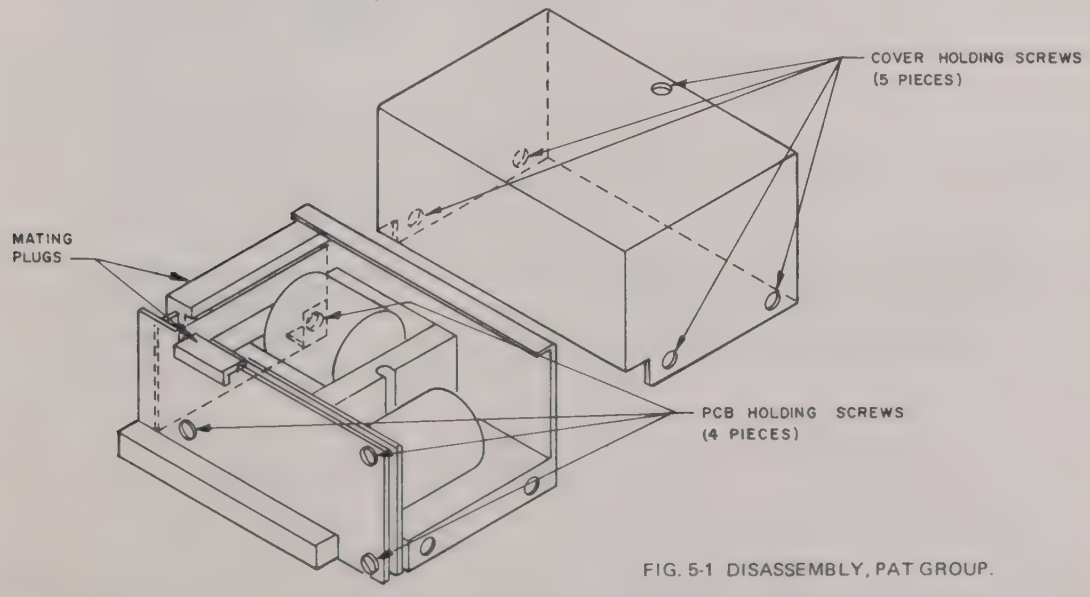


FIG. 5-1 DISASSEMBLY, PAT GROUP.

5-7 INSTALLATION OF OPTIONAL CONTROL COMPONENTS

5-8 Disassembly of the printed circuit board: The installation of the optional components can be readily accomplished by first removing the printed circuit board from the chassis assembly.

a) Remove the (4) mounting screws as indicated on FIG. 5-1. Remove printed circuit board connectors and lift PCB from assembly.

b) Install components as directed in the following paragraphs and as shown in FIG. 5-2.

c) Reinstall PCB, replace mounting screws and proceed with alignment procedures.

5-9 INSTALLATION AND CALIBRATION OF BRIDGE CURRENT ADJUSTMENT (R16): REFER TO FIG. 5-2.

NOTE: Models which have this control factory-installed bear the suffix "R" with their model designation [e.g., Model PAT 7–2R]. The installation notes may be disregarded with these models.

a) Remove jumper between terminals marked "A" and "B."

b) Insert 1K ohm, metal film potentiometer in the space outlined on FIG. 5-2. Refer to parts list in Section VI for correct part number. Solder in place.

c) Turn potentiometer to its approximate midposition. Install a precision resistor (R_{VC}) across barrier-strip terminals (11) and (12) and connect a precision voltmeter across the output terminals (5) and (6). Since the bridge current (I_b) is a nominal 1 mA and the control ratio is 1 K ohm/V, the output voltage E_O will be approximately equal to $E_O = I_b R_{VC}$. [i.e., for each 1000 ohms of voltage control resistance (R_{VC}), 1 volt of output voltage will appear across the output terminals.]

d) Turn unit on and read the output voltage. Adjust R16 until E_O is exactly equal to the number of kilohms of R_{VC}. The accuracy of the adjustment is limited only by the tolerances of R_{VC} and by the accuracy of the voltmeter across the output terminals.

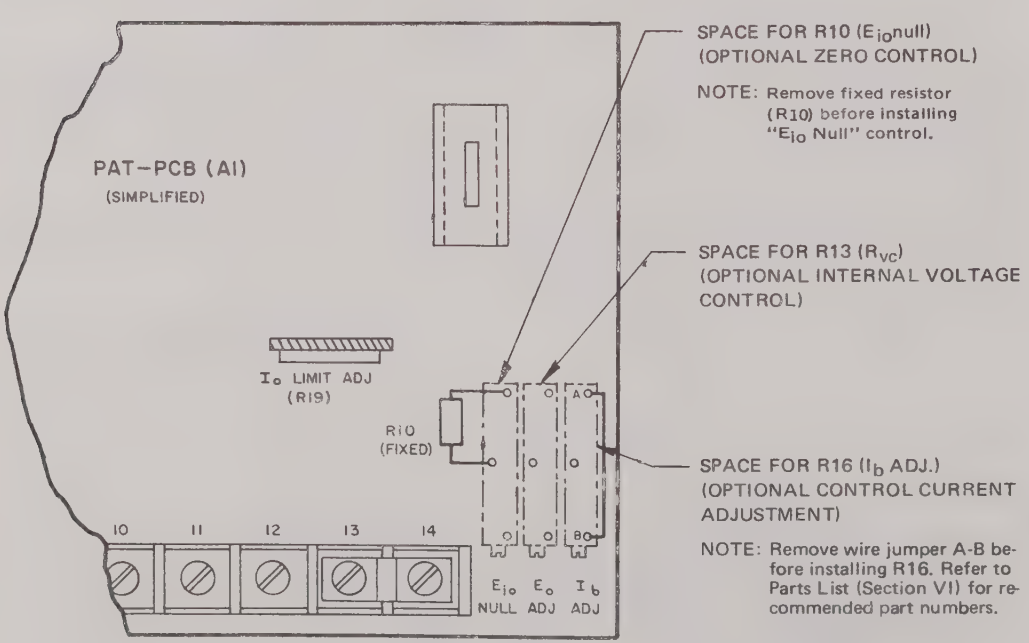


FIG. 5-2 INSTALLATION OF OPTIONAL CONTROLS.

5-10 INSTALLATION AND ADJUSTMENT OF THE (OPTIONAL) VOLTAGE CONTROL POTENTIOMETER (R_{VC}): REFER TO FIG. 5-2.

NOTE: Models which have this control factory-installed bear a 4 or 5 digit number following their model designation (e.g., "Model PAT 7–2–8000."). The installation notes may be disregarded with these units.

a) Refer to FIG. 5-2. The voltage control resistance (R_{VC}) may be installed internally in the space on the printed circuit board as shown.

b) The value choice of the voltage control resistance will depend on the application. For each 1000 ohms, 1 volt will be produced. Good quality metal film potentiometers should be selected. Refer to Amphenol Series 2600P or Bourns 3009P miniature trim potentiometers.

c) After installation of the desired component, the power supply is adjusted to the chosen output voltage by varying R13 (and/or R16) and monitoring the output with a precision voltmeter.

5-11 INSTALLATION AND ADJUSTMENT OF THE (OPTIONAL) ZERO CONTROL: REFER TO FIG. 5-2.

NOTE: Models which have this control factory-installed bear the suffix "E" [e.g., Model PAT 7–2E]. The installation notes may be disregarded with these models.

a) Remove fixed resistor (R10) from the printed circuit board. Insert R10, 10 K metal film potentiometer in the space outlined on FIG. 5-2. Refer to parts list in Section VI for correct part number. Solder in place.

b) Place a short circuit between terminals (11)–(12), and connect a sensitive millivoltmeter across the output terminals (5)–(6), and adjust R10 until zero volts are read out on the meter. Remove short and millivoltmeter.

5-12 TROUBLE SHOOTING

5-13 Modern, high performance power supplies have reached a state of sophistication that requires thorough understanding of the problems involved in repairing complex solid-state circuitry. Servicing beyond simple parts replacements should consequently be attempted only by personnel thoroughly familiar with solid-state component techniques and with experience in closed-loop circuitry.

5-14 Trouble shooting charts showing resistance and voltage readings are of very limited usefulness with feedback amplifiers and are not included here. Instead, a detailed circuit description (Section IV), parts location diagrams, simplified functional schematics, and a main schematic with all significant voltage readings are presented.

5-15 The following basic steps in a case of power supply malfunctioning may also be found useful:

a) If power supply does not function at all, check all power source connections and make certain power supply is connected to the correct source voltage. (It is normally delivered for 115V a-c operation.)

b) If supply is used as an operational amplifier with external feedback components, disconnect all external components and reconnect internal reference and the voltage control. The following paragraphs refer to the instrument as a power supply, rather than an amplifier in order to simplify test setups and measurements.

c) If the power supply is basically functioning, but poor performance is evident, inspect the test setup to make certain the source of the trouble is not external.

d) Excessive output variations in any of the operating modes are usually traceable to incorrectly connected loads or faulty measurement techniques. Perform measurements are described in paragraph 5-17 and follow the connecting diagram.

e) Oscillation of the output voltage or output current is often due to a load with a large inductive component. Twisted load wires of sufficient diameter and held as short as practical are often the solution to the problem.

16 Kepco Field Engineering Offices or the Kepco Repair Department will be available for consultation or direct help in difficult service or application problems.

5-17 POWER SUPPLY MEASUREMENTS

5-18 Measurement of the significant parameters of a power supply is a requirement for incoming inspection, periodic maintenance, or after component replacement. Since the measurements require special techniques to insure correct results, suggestions for their performance are given below:

a) Required Instrumentation:

1) Constant a-c source voltage with provisions for "stepping" the voltage over a specified region (105–125V); a variable autotransformer is generally adequate if it is rated to deliver the input current of the unit under test.

2) Resistive load, variable, with ON/OFF and shorting switch and capable of dissipating the full output power of the unit under test.

3) D-C voltage monitor, differential voltmeter or power supply analyzer.

4) Current sensing resistor for measurements of output effects in the current mode, 4-terminal device.

5) A-C ripple monitor, sensitivity better than 1 mV. Ballantine Model 302C or Hewlett Packard Model 400H.

6) Optional: Oscilloscope, vertical sensitivity better than 0.1 mV/cm.

b) The 4-terminal network is an essential device for precise measurement of regulation. It can be approximated by the method shown in FIG. 5-4.

FIG. 5-3 shows the proper location of instrument leads when measuring output effects. The principle governing this method of measurement is simply ***not*** to measure any voltage drops due to load current flow. This can only be avoided by measuring output effects either directly at the sensing leads or by the use of the 4-terminal network.

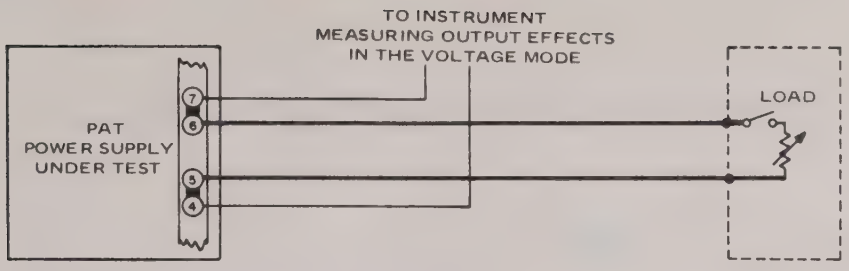
c) Output effects due to source or load variations in the voltage mode are defined as the amount of output voltage change resulting from a specified change of source voltage or from a change in load resistance. The output effects can be expressed as an absolute change (ΔE_O) or as a percentage in reference to the total output voltage E_O:

$$\% \text{ Output Effect} = \frac{\Delta E_O}{E_O} \times 100\%$$

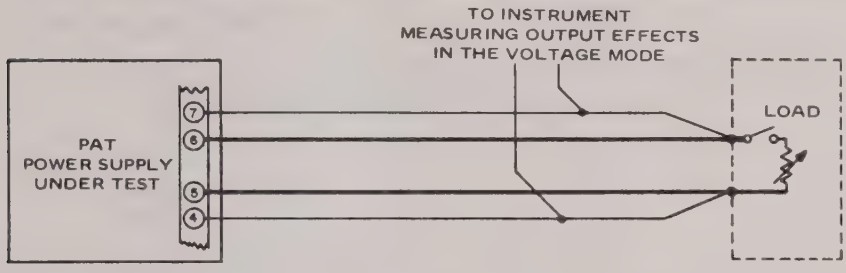
d) Output effects due to source or load variations in the current mode are defined as the amount of output current change resulting from a specified change in source voltage or from a change in load resistance. The output effects can be expressed as an absolute change (ΔI_O) or as a percentage in reference to the total output current I_O:

$$\% \text{ Output Effect} = \frac{\Delta I_O}{I_O} \times 100\%.$$

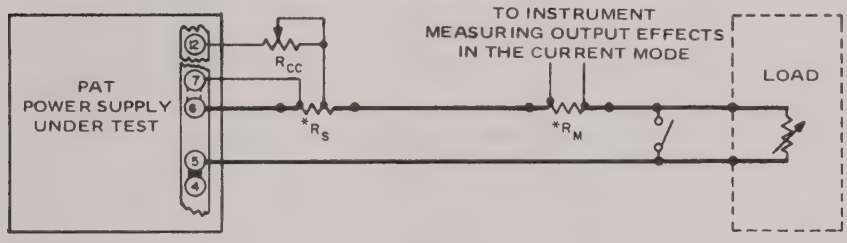
e) Ripple: rms ripple may be monitored on a true rms reading instrument connected parallel to the instrument measuring the output effects. Careful wire dressing and shielding, as well as good a-c grounding, are of the utmost importance if valid measurements are being expected. An oscilloscope may also be used for p-p readings of noise and ripple. An approximate rms reading can be calculated from the p-p reading of the oscilloscope if the reading is divided by three.



A) OUTPUT EFFECT MEASUREMENT (VOLTAGE) WITHOUT ERROR SENSING AT THE LOAD.



B) OUTPUT EFFECT MEASUREMENT (VOLTAGE) WITH ERROR SENSING AT THE LOAD.



C) OUTPUT EFFECT MEASUREMENT (CURRENT).

*R_S and R_M should be selected such that their wattage rating is at least 10 times the actual power dissipated. All output effects depend on the stability of R_S and R_M. Recommended T.C. is 20 ppm. In addition, R_S and R_M must be of the "four terminal network" type.

FIG. 5-3 POWER SUPPLY MEASUREMENTS, CONNECTING METHODS.

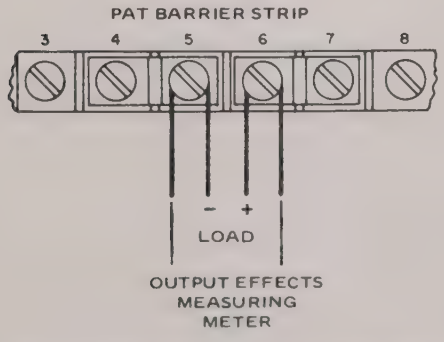


FIG. 5-4 APPROXIMATION OF A 4-TERMINAL NETWORK.

SECTION VI – ELECTRICAL PARTS LIST AND DIAGRAMS

6-1 GENERAL

6-2 This section contains the main schematic, the parts location diagrams, and a list of all replaceable electrical parts. All components are listed in alpha-numerical order of their reference designations. Consult your Kepco Representative for replacement of parts not listed here.

6-3 ORDERING INFORMATION

6-4 To order a replacement part or to inquire about parts not listed in the parts list, address order or inquiry either to your authorized Kepco Sales Representative or to:

KEPCO, INC.
131–38 Sanford Avenue
Flushing, N.Y. 11352

6-5 Specify the following information for each part:

a) Model and complete serial number of instrument.

b) Kepco part number.

c) Circuit reference designator.

d) Description.

6-6 To order a part not listed in the parts list, give a complete description and include its function and location.

PRINTED CIRCUIT BOARD ASSEMBLY (A1), MODEL PAT 15–1.5(E) (R) (T) 5-17-71

SCHEMATIC NO.	QTY.	DESCRIPTION	MFRS. NAME & PART NO. (SEE NOTE)	KEPCO PART NO.
A_V	1	Integrated Circuit Amplifier	Kepco 250-0025	250-0025
C1	1	Capacitor,Electrolytic,Pigtail 75 μF, 50V	Sprague 30D	117-0646
C2	1	Capacitor,Mylar,Pigtail 0.0047 μF, 200V	Wesco Type 32M	117-0588
C4	1	Capacitor,Mylar,Pigtail 0.2 μF, 200V	TRW X663F	117-0236
C6	1	Capacitor,Electrolytic,Pigtail 290 μF, 12V	Sprague 30D	117-0647
C7,10	2	Capacitor,Electrolytic,Pigtail 150 μF, 3V	General Electric Type 76F	117-0512
C8	1	Capacitor,Electrolytic,Pigtail 300 μF, 40V	Sprague Type 38D	117-0654
C9	1	Capacitor,Mylar,Pigtail 0.1 μF, 600V	TRW X663F	117-0316
CR1,2, 9,10,11, 12,13,14	8	Rectifier,Silicon,Pigtail	Semtech SE-1	124-0133
CR3,4, 5,6,7	5	Zener Diode	Transitron 1N763-1	121-0028
CR8	1	Zener Diode	International Resistance 1N821	121-0041
*CR8	1	Zener Diode	International Rectifier 1N821	121-0062
CR15,16,17, 18,19,20,21	7	Rectifier,Silicon,Pigtail	Semicon S5A1	124-0347
CR22,23	2	Rectifier,Silicon,Pigtail	EDI CEC 1000	124-0178
F1	1	Fuse,Instantaneous 1.0 Amp	Bussman AGX-1	141-0072
Q1,4,5	3	Transistor,NPN	M.S. Transistor 119-0094	119-0094
Q2,3	2	Transistor,NPN	Fairchild 2N5133	119-0093
R1	1	Resistor,Fixed,Precision,M.F. 232 ohm, 1/4 W, 1%	IRC CEA Type	115-2310
R2	1	Resistor,Fixed,M.G. 82 K ohm, 1/4 W, 5%	IRC RG-1/4	115-2012
R3	1	Resistor,Fixed,Molded 3.9 K ohm, 1/2 W, 5%	Allen Bradley EB3925	115-0808
R4	1	Resistor,Fixed,Molded 1 K ohm, 1/4 W, 10%	IRC GBT-1/4	115-2238

*Models with suffix "T" only. (Kepco Part No. 121-0041 is deleted.)

NOTE:
REPLACEMENT PARTS MAY BE ORDERED FROM KEPCO, INC. ORDERS SHOULD INCLUDE KEPCO PART NUMBER AND DESCRIPTION.

PLEASE NOTE: THE MANUFACTURER'S NAME AND PART NUMBER LISTED FOR EACH ITEM ON REPLACEMENT PARTS LISTS, REPRESENTS AT LEAST ONE SOURCE FOR THAT ITEM AND IS LISTED SOLELY FOR THE CONVENIENCE OF KEPCO EQUIPMENT OWNERS IN OBTAINING REPLACEMENT PARTS LOCALLY. WE RESERVE THE RIGHT TO USE EQUIVALENT ITEMS FROM ALTERNATE SOURCES. KEPCO, INC.

PRINTED CIRCUIT BOARD ASSEMBLY (A1), MODEL PAT 15–1.5(E) (R) (T) 5-17-71

SCHEMATIC NO.	QTY.	DESCRIPTION	MFRS. NAME & PART NO. (SEE NOTE)	KEPCO PART NO.
R5	1	Resistor,Fixed,Precision,M.F. 750 ohm, 1/4 W, 1%	IRC CEA Type	115-2259
R6	1	Resistor,Fixed,Molded 47 K ohm, 1/4 W, 10%	IRC GBT-1/4	115-2237
R7	1	Resistor,Fixed,M.G. 6.2 K ohm, 1/4 W, 5%	IRC GBT-1/4	115-2258
R8	1	Resistor,Fixed,Molded 10 K ohm, 1/4 W, 10%	IRC GBT-1/4	115-2211
R10	1	Resistor,Fixed,Precision,M.F. 1.5 K ohm, 1/4 W, 1%	IRC CEA Type	115-2178
**R10	1	Resistor,Variable,Cermet 10 K ohm, 3/4 W, 10%	Bourns 3009P-1-103	115-2481
R11	1	Resistor,Fixed,Molded 1.5 K ohm, 1/4 W, 10%	IRC GBT-1/4	115-2229
R12,17	2	Resistor,Fixed,Molded 560 ohm, 1/4 W, 10%	IRC GBT-1/4	115-2210
R15	1	Resistor,Fixed,Precision,WW 5.7 K ohm, 1 W, 1%	Tepro TS-1W	115-1301
***R16	1	Resistor,Variable,WW 1 K ohm, 1 W, 10%	Bourns 3007P-1-102	115-2264
R18	1	Resistor,Fixed,Precision,M.F. 10 K ohm, 1/4 W, 1%	IRC CEA Type	115-2174
R19	1	Resistor,Variable,Composition 1.5 K ohm, 1/8 W, 30%	C.T.S. X201	115-1939
R20	1	Resistor,Fixed,Power,Axial 1.5 K ohm, 3 W, 5%	Tepro TS-3W	115-1189
R21	1	Resistor,Fixed,Molded 39 ohm, 1/4 W, 10%	IRC GBT-1/4	115-2501
R22,24	2	Resistor,Fixed,Power,Axial 0.5 ohm, 3 W, 3%	Tepro TS-3W	115-2132
R23	1	Resistor,Fixed,Molded 120 ohm, 1/4 W, 10%	IRC GBT-1/4	115-2232
R25	1	Resistor,Fixed,Molded 750 ohm, 1 W, 5%	Allen Bradley GB7515	115-2438
R26	1	Resistor,Fixed,Power,Axial 0.33 ohm, 3 W, 3%	Tepro TS-3W	115-1873
R27	1	Resistor,Fixed,Power,Axial 300 ohm, 3 W, 5%	Tepro TS-3W	115-0444
R28	1	Resistor,Fixed,Molded 10 ohm, 1/4 W, 10%	IRC GBT-1/4	115-2230

**Models with suffix "E" only, R10, Kepco Part No. 115-2178 is deleted.
***Models with suffix "R" only.

NOTE:
REPLACEMENT PARTS MAY BE ORDERED FROM KEPCO, INC. ORDERS SHOULD INCLUDE KEPCO PART NUMBER AND DESCRIPTION.

PLEASE NOTE: THE MANUFACTURER'S NAME AND PART NUMBER LISTED FOR EACH ITEM ON REPLACEMENT PARTS LISTS, REPRESENTS AT LEAST ONE SOURCE FOR THAT ITEM AND IS LISTED SOLELY FOR THE CONVENIENCE OF KEPCO EQUIPMENT OWNERS IN OBTAINING REPLACEMENT PARTS LOCALLY. WE RESERVE THE RIGHT TO USE EQUIVALENT ITEMS FROM ALTERNATE SOURCES. KEPCO, INC.

MAIN CHASSIS ASSEMBLY (A2), MODEL PAT 15–1.5(E) (R) (T) 5-17-71

SCHEMATIC NO.	QTY.	DESCRIPTION	MFRS. NAME & PART NO. (SEE NOTE)	KEPCO PART NO.
C201	1	Capacitor,Electrolytic,Can 2000 μF, 40V d-c	General Electric Type 86F	117-0660
Q201,202	2	Transistor,NPN	Westinghouse 2N3055	119-0061
T201	1	Transformer,Power	Kepco 100-1877	100-1877

NOTE:

REPLACEMENT PARTS MAY BE ORDERED FROM KEPCO, INC. ORDERS SHOULD INCLUDE KEPCO PART NUMBER AND DESCRIPTION.

PLEASE NOTE: THE MANUFACTURER'S NAME AND PART NUMBER LISTED FOR EACH ITEM ON REPLACEMENT PARTS LISTS, REPRESENTS AT LEAST ONE SOURCE FOR THAT ITEM AND IS LISTED SOLELY FOR THE CONVENIENCE OF KEPCO EQUIPMENT OWNERS IN OBTAINING REPLACEMENT PARTS LOCALLY. WE RESERVE THE RIGHT TO USE EQUIVALENT ITEMS FROM ALTERNATE SOURCES. KEPCO, INC.

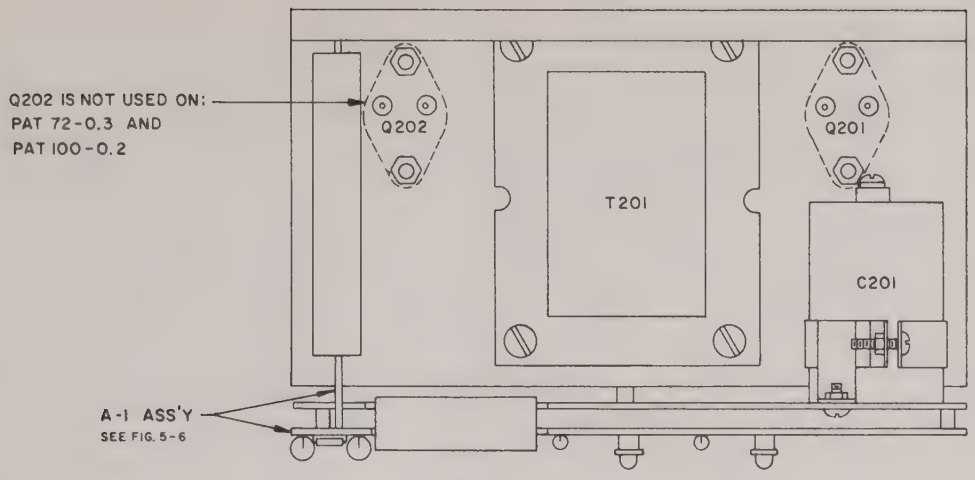
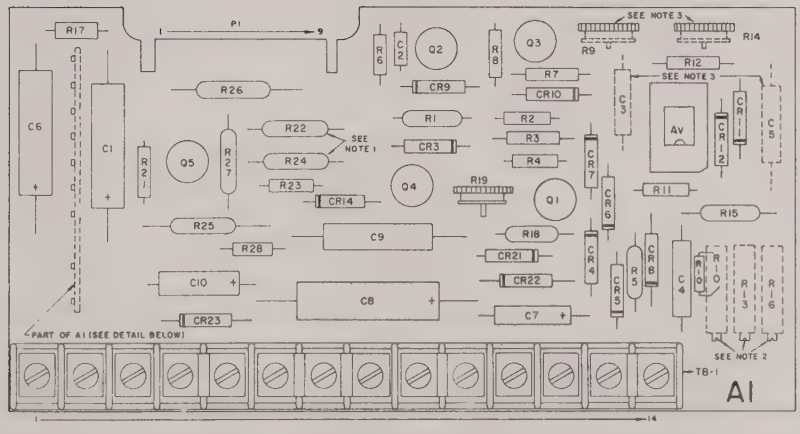
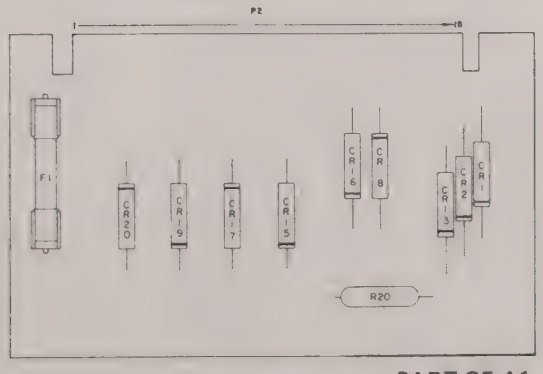


FIG. 6-1 COMPONENT LOCATION,
MAIN CHASSIS ASSEMBLY (A2).



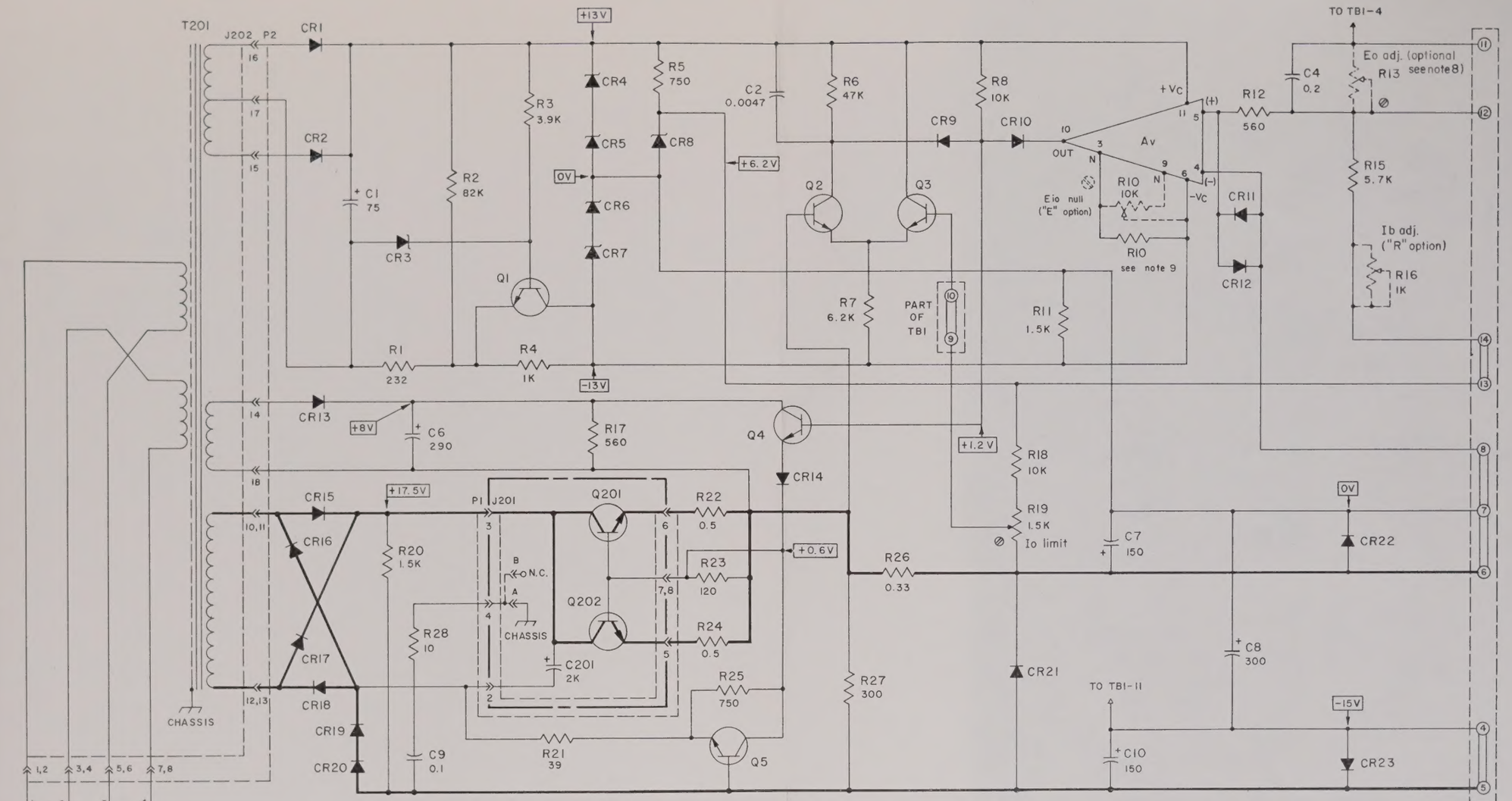
NOTES
1 R22 AND R24 ARE NOT USED ON PAT 72-0.3 AND PAT 100-0.1
2 R10 IS USED ONLY IN MODELS WITH SUFFIX "E", R16 IS USED ONLY IN MODELS WITH SUFFIX "R" AND R13 IS USED ONLY IN MODELS WITH FIVE PLACE NUMERICAL SUFFIX R10(FIXED RESISTOR, SHOWN WITH SOLID LINES)IS USED ONLY IN MODELS WITHOUT SUFFIX "E"
3 C3, C5, R9 AND R14 ARE NOT USED IN ALL MODELS
REFER TO MAIN SCHEMATIC

FIG. 6-2 COMPONENT LOCATION,
PRINTED CIRCUIT BOARD (A1).

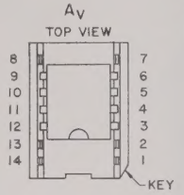


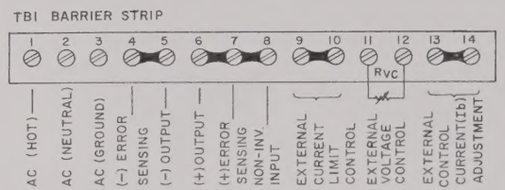
PART OF A1

NOTE CR16 THRU CR20 ARE MOUNTED ON LUGS
ON THE MODELS PAT 7-2, PAT 15-1.5 AND PAT 21-1



NOTES:
1. RESISTOR VALUES IN OHMS, K=1000.
2. CAPACITOR VALUES IN MICROFARADS, UNLESS OTHERWISE INDICATED.
3. CONNECTOR MARKED WITH CAPITAL LETTERS ARE "QUICK DISCONNECT" TERMINALS.
4. CONDITIONS FOR VOLTAGE READINGS:
   A) INPUT 115 V AC. RESISTOR (RVC) BETWEEN TERMINALS (11) AND (12) ON TBI, TO PRODUCE MAX. RATED OUTPUT VOLTAGE. (Rvc = 1000 OHMS PER VOLT)
   B) METER: WESTON MODEL 980 ANALYZER (20K OHMS/VOLT.)
   C) READINGS SUBJECT TO NORMAL COMPONENT TOLERANCES, AND REFERRED TO THE POSITIVE (+) ERROR SENSING TERMINAL.
5. MAIN TRANSFORMER (T201) CONNECTED FOR 115 VAC OPERATION.
6. COMPONENT NUMBERS 1 TO 99 LOCATED ON PRINTED CIRCUIT BOARD (A-1). COMPONENT NUMBERS 201 TO 299 LOCATED ON MAIN CHASSIS (A-2)
7. LAST COMPONENT NUMBERS USED: C10, CR23, Q5, R28 (A-1); C201, Q202, T201 UNUSED COMPONENT NUMBERS: C3, C5, R9, R14
8. VALUE OF R13 DEPENDING ON NUMERICAL SUFFIX TO PAT MODEL NUMBER
9. FIXED RESISTOR (R10) USED ONLY ON MODELS WITHOUT "E" OPTION.





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Data subject to change without notice
PATENT NOTICE: Applicable Patent Nos. will be supplied on request.

FIG. 6-3 MAIN SCHEMATIC, MODEL PAT 15-1.5

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INSTRUCTION MANUAL CORRECTION

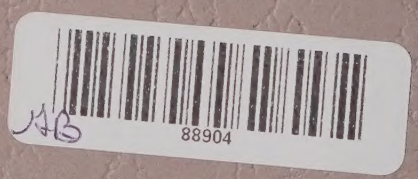
Please note the following corrections to the Manual material as indicated:

Parts List and Schematic Diagram:

1. CHANGE:	Q1	Transistor, Silicon, NPN		Kepco #119-0094 to:
	Q1	Transistor, Silicon, NPN		Kepco #119-0059.
2. ADD:	C11	Capacitor, Ceramic, Disc	.005MF, 500V	Kepco #117-0061.
3. DELETE:	R4	Resistor, Fixed, Molded	1KΩ, ¼W, 10%	Kepco #115-2238.

Note: C11 has been added to assembly A1. It is connected between the collector and emitter of Q1.

PAT 15-1.5/9-2072/r10 C2992



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